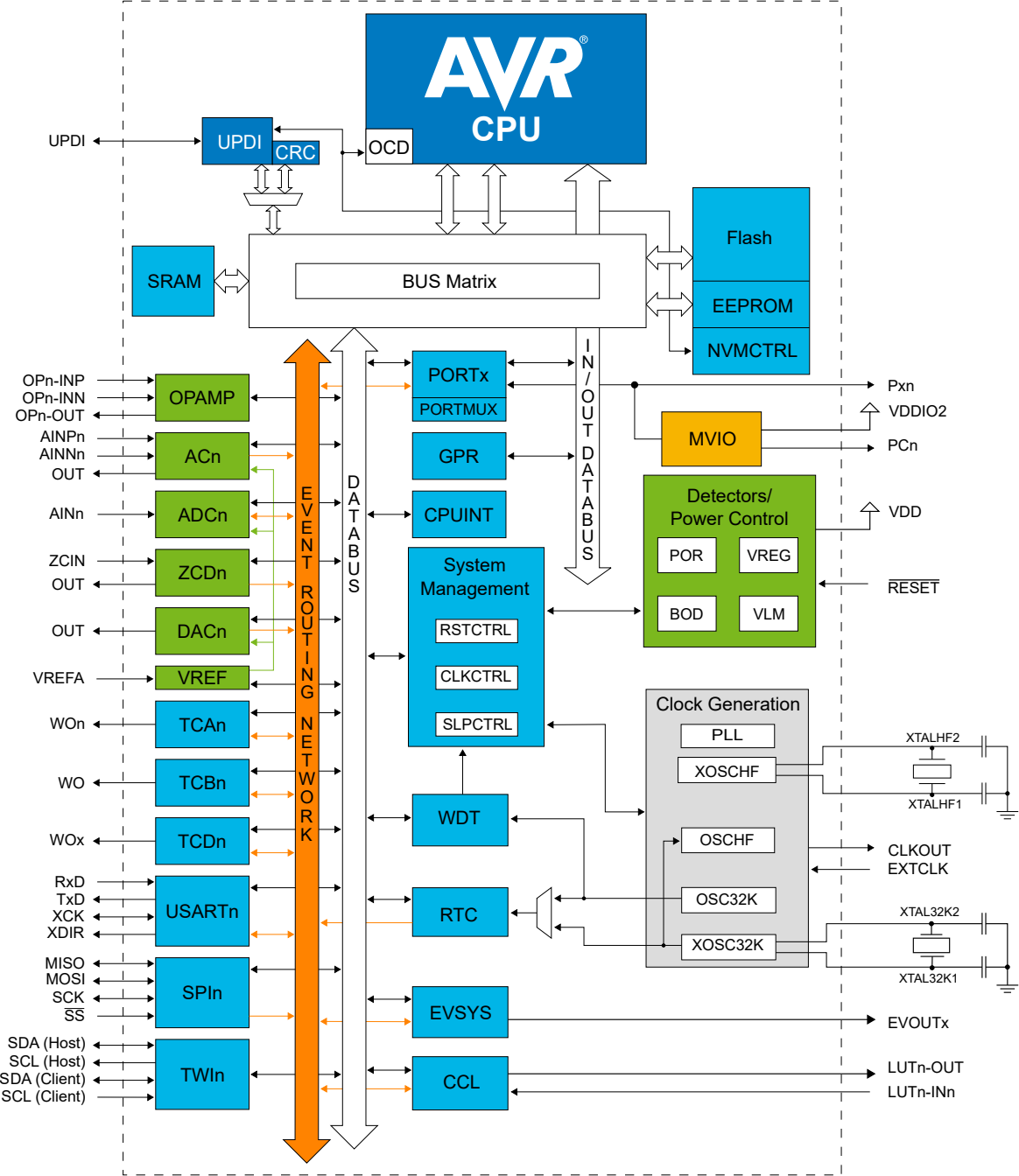
AVR
CPU
UPDI
UPDI
CRC
OCD
SRAM
BUS Matrix
Flash
EEPROM
NVMCTRL
OPn-INP
OPn-INN
OPn-OUT
OPAMP
AINPn
AINNn
OUT
ACn
AINn
ADCn
ZCIN
OUT
ZCDn
OUT
DACn
VREFA
VREF
WOn
TCAn
WO
TCBn
WOx
TCDn
RxD
TxD
XCK
XDIR
USARTn
MISO
MOSI
SCK
SS
SPIn
SDA (Host)
SCL (Host)
SDA (Client)
SCL (Client)
TWIn
EVENT ROUTING NETWORK
DATABUS
PORTx
PORTMUX
GPR
CPUINT
System Management
RSTCTRL
CLKCTRL
SLPCTRL
WDT
RTC
EVSYS
CCL
IN/OUT DATABUS
Pxn
VDDIO2
MVIO
PCn
Detectors/ Power Control
POR
VREG
BOD
VLM
VDD
RESET
Clock Generation
PLL
XOSCHF
OSCHF
OSC32K
XOSC32K
XTALHF2
XTALHF1
CLKOUT
EXTCLK
XTAL32K2
XTAL32K1
EVOUTx
LUTn-OUT
LUTn-INn