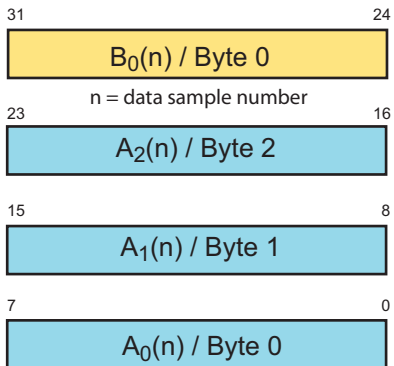
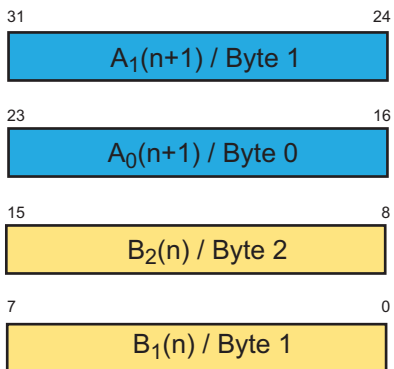
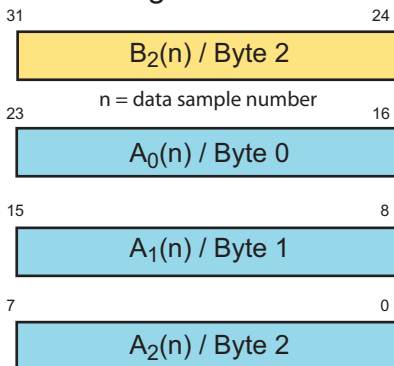


Little Endian

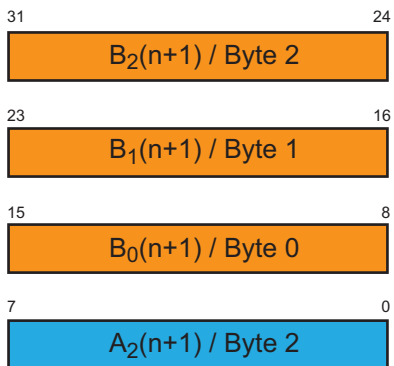
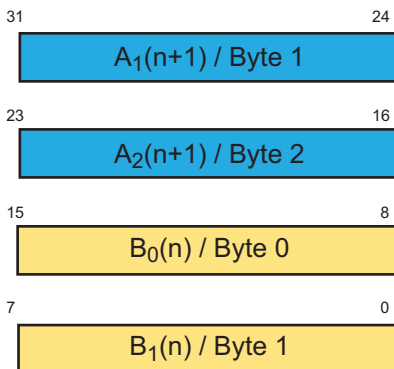


CDR
access
cycle
 $3 \cdot p$
where p
is 0,1,2 etc

Big Endian



CDR
access
cycle
 $3 \cdot p + 1$
where p
is 0,1,2 etc



CDR
access
cycle
 $3 \cdot p + 2$
where p
is 0,1,2 etc

