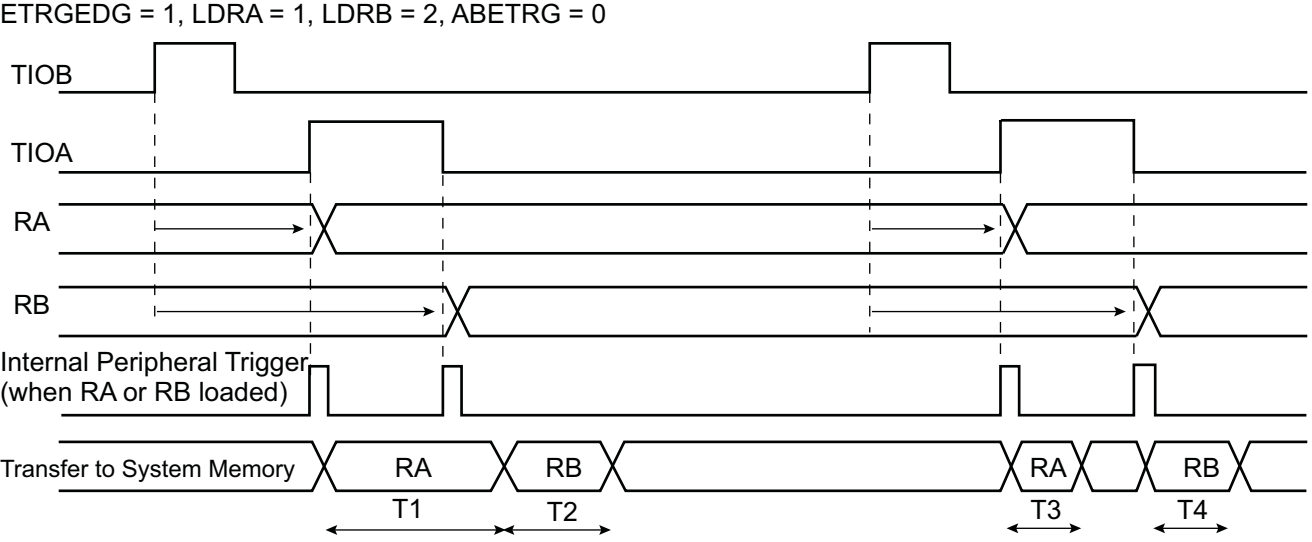
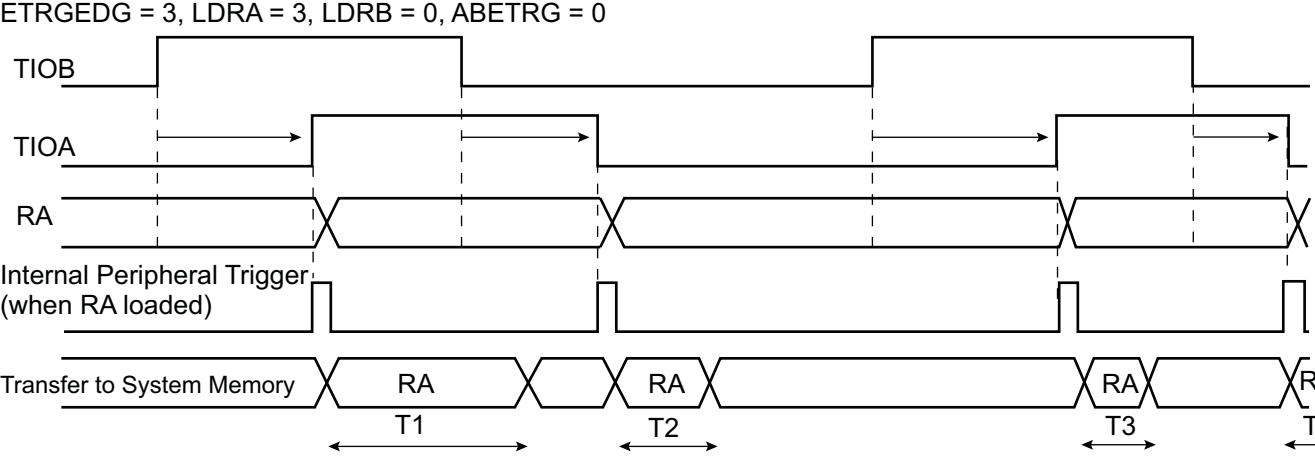


T1,T2,T3,T4 = System Bus load dependent (t_{min} = 8 Peripheral Clocks)



T1,T2,T3,T4 = System Bus load dependent (t_{min} = 8 Peripheral Clocks)