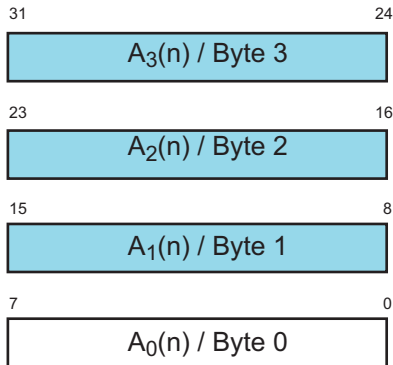
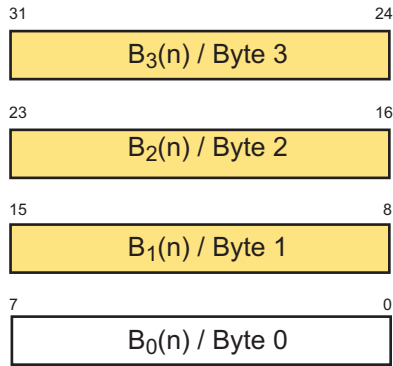
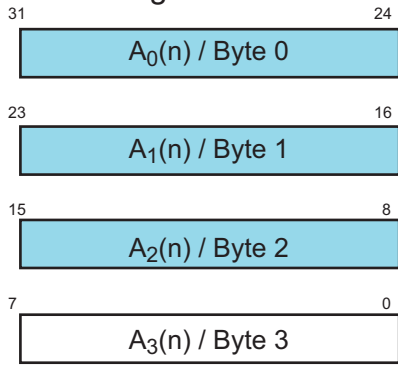


Little Endian



CDR
access
cycle
M
where
 $M=2*n$

Big Endian



CDR
access
cycle
M+1
where
 $M=2*n$

