

DDRRC Command Path

Input commands

Input Handler

HPR Bypass Cmds

BYPASS ENGINE

HPR Rd and Act cmds

Page Table Info, Collision etc

Bypass match/disable

PHY Interface

DFI Cmd/Addr Output

HPR/LPR/WR Cmds through CAM

Col, Row, token info

Scheduled Op, Rank, Bank info

Transaction Engine
(CAMs, Next Transaction Table)

Transaction Scheduler
(BSM, Operation Selection, Global Selection)

