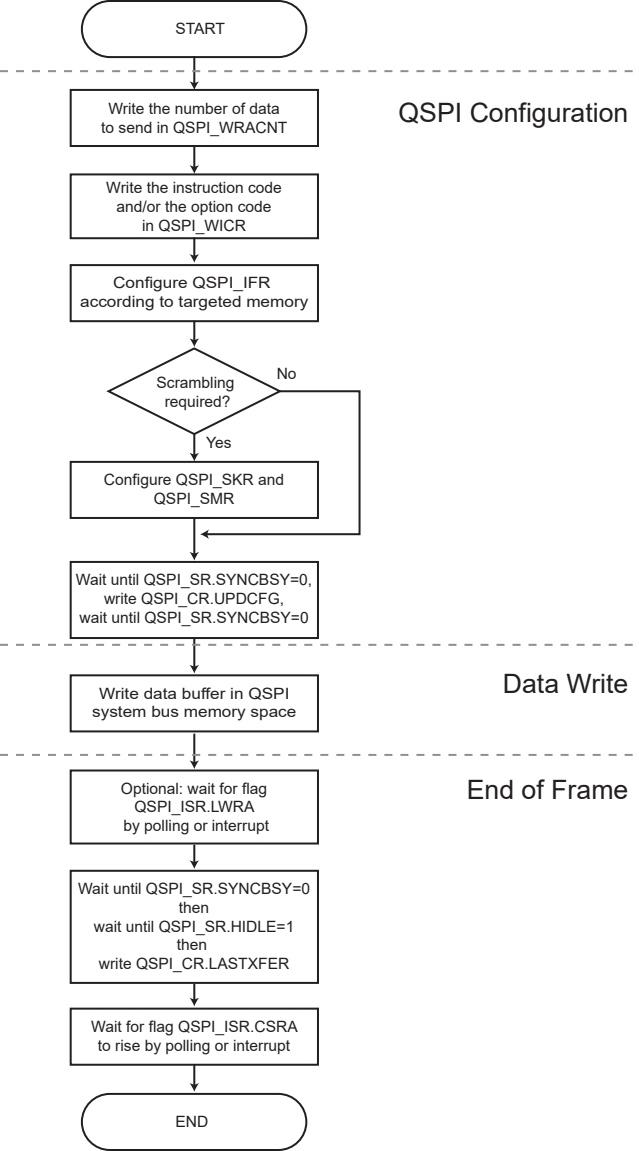
START
QSPI Configuration
Write the number of data to send in QSPI_WRACNT
Write the instruction code and/or the option code in QSPI_WICR
Configure QSPI_IFR according to targeted memory
Scrambling required?
No
Yes
Configure QSPI_SKR and QSPI_SMR
Wait until QSPI_SR.SYNCBSY=0, write QSPI_CR.UPDCFG, wait until QSPI_SR.SYNCBSY=0
Data Write
Write data buffer in QSPI system bus memory space
End of Frame
Optional: wait for flag QSPI_ISR.LWRA by polling or interrupt
Wait until QSPI_SR.SYNCBSY=0 then wait until QSPI_SR.HIDLE=1 then write QSPI_CR.LASTXFER
Wait for flag QSPI_ISR.CSRA to rise by polling or interrupt
END