



START

Write HyperFlash™  
"Write to Buffer" commands

Write the address in QSPI\_IAR

Configure QSPI\_IFR  
according to the targeted memory  
(HFWBEN set to '1')

Scrambling  
required?

Configure QSPI\_SKR  
and QSPI\_SMR

Wait until QSPI\_SR.SYNCBSY=0,  
write QSPI\_CR.UPDCFG,  
wait until QSPI\_SR.SYNCBSY=0

Wait until flag QSPI\_ISR.TDRE=1  
by polling or interrupt

Write QSPI\_TDR

Last data  
to write?

Wait for flag QSPI\_ISR.TXEMPTY  
by polling or interrupt

Wait until QSPI\_SR.SYNCBSY=0  
then  
write QSPI\_CR.LASTXFER

Wait for flag QSPI\_ISR.CSRA  
to rise by polling or interrupt

Write HyperFlash™  
"Program Buffer to Flash  
(confirm)" command

END

QSPI Configuration

Data Write

End of Frame