START

QSPI Configuration

Write the address in QSPI_IAR

Write the instruction code and/or the option code in QSPI_WICR

Configure QSPI_IFR according to the targeted memory

Scrambling required? — No

Yes

Configure QSPI_SKR and QSPI_SMR

Wait until QSPI_SR.SYNCBSY=0, write QSPI_CR.UPDCFG, wait until QSPI_SR.SYNCBSY=0

Data Write

Wait until flag QSPI_ISR.TDRE=1 by polling or interrupt

Write QSPI_TDR

Last data to write? — No

Yes

End of Frame

Wait for flag QSPI_ISR.TXEMPTY by polling or interrupt

Wait until QSPI_SR.SYNCBSY=0 then write QSPI_CR.LASTXFER

Wait for flag QSPI_ISR.CSRA to rise by polling or interrupt

END