



① Setting the SEN bit begins a Start event.

② Writing the I2CxTRN register starts a host transmission. The data is the address byte with the R/W status bit clear.

③ Writing the I2CxTRN register starts a host transmission. The data is the message byte.

④ Setting the PEN bit starts a host Stop event.

⑤ Setting the SEN bit begins a Start event. An interrupt is generated on completion of the Start event.

⑥ Writing the I2CxTRN register starts a host transmission. The data is the address byte with the R/W status bit set.

⑦ Setting the RCEN bit starts a host reception.

⑧ Setting the ACKEN bit starts an Acknowledge event. ACKDT = 1 to send a NACK.

⑨ Setting the PEN bit starts a host Stop event.