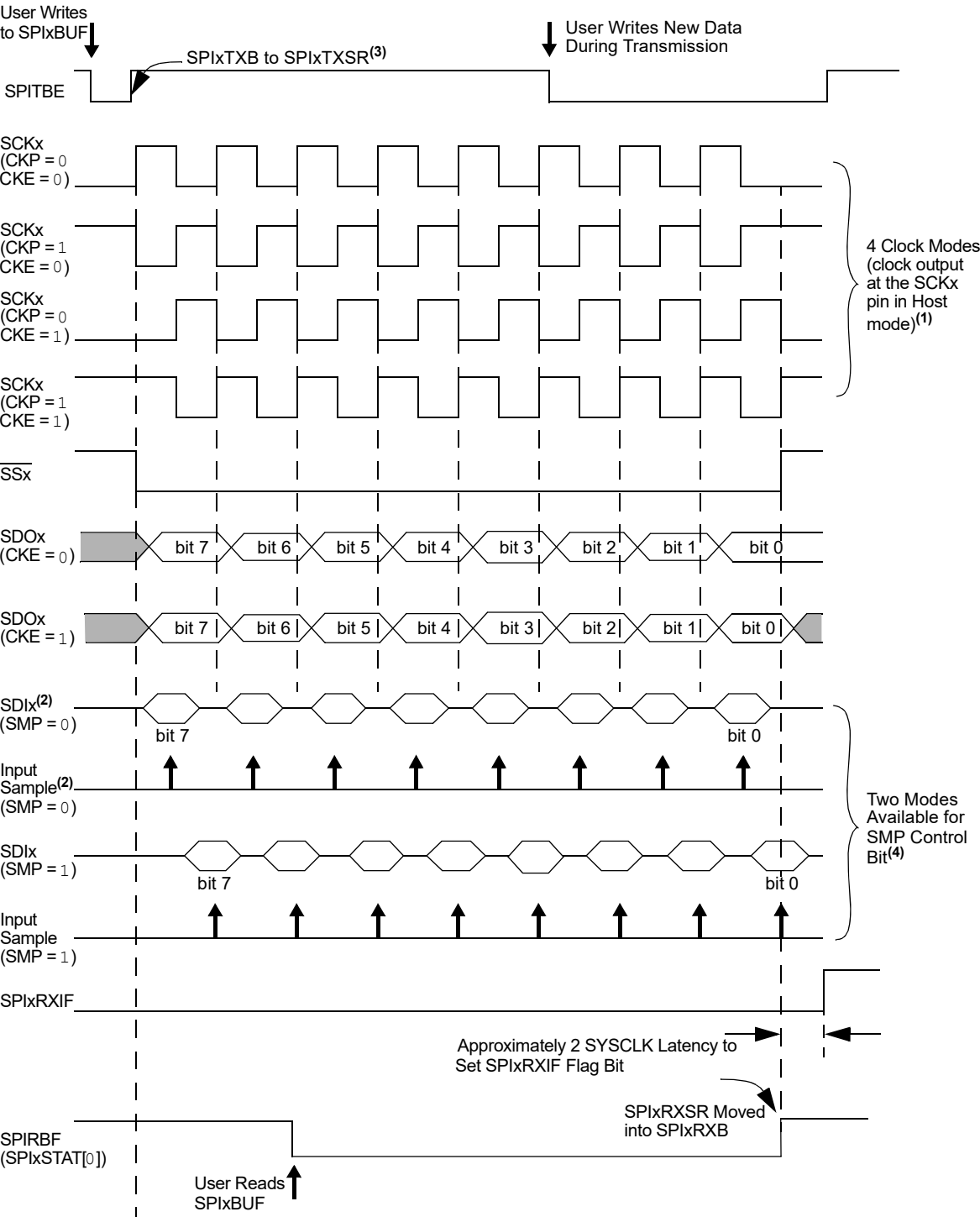
User Writes to SPIxBUF
SPIxTXB to SPIxTXSR(3)
User Writes New Data During Transmission
SPITBE
SCKx (CKP = 0 CKE = 0)
SCKx (CKP = 1 CKE = 0)
SCKx (CKP = 0 CKE = 1)
SCKx (CKP = 1 CKE = 1)
4 Clock Modes (clock output at the SCKx pin in Host mode)(1)
SSx
SDOx (CKE = 0)
SDOx (CKE = 1)
bit 7
bit 6
bit 5
bit 4
bit 3
bit 2
bit 1
bit 0
SDIx(2) (SMP = 0)
Input Sample(2) (SMP = 0)
SDIx (SMP = 1)
Input Sample (SMP = 1)
Two Modes Available for SMP Control Bit(4)
SPIxRXIF
Approximately 2 SYSCLK Latency to Set SPIxRXIF Flag Bit
SPIxRXSR Moved into SPIxRXB
SPIRBF (SPIxSTAT[0])
User Reads SPIxBUF