

SOC

SOC

MPER-

PWM Timer

0.

PG1STAT
CAHALF

1

0

PWM1H

PG1DC

PWM1L

PG1DTH

PG1DTL

PG2STAT
CAHALF

1

0

PWM2H

PG2DC

PWM2L

PG2DTH

PG2DTL

PG3STAT
CAHALF

1

0

PWM3H

PG3DC

PWM3L

PG3DTH

PG3DTL

Timer Cycle n

Timer Cycle n + 1

PWM Cycle

