



- ① Detecting Start bit enables address detection. If SCIE is set, then interrupt will be asserted.<sup>(1)</sup>
- ② User software clears the interrupt flag.
- ③  $R/\bar{W} = 1$  indicates that the client sends data bytes.
- ④ Valid address of first byte clears  $D/\bar{A}$  status bit. Client generates  $\bar{ACK}$ .
- ⑤  $R/\bar{W}$  status bit is set. Client generates interrupt. SCLREL is cleared. Client pulls SCLx low while SCLREL = 0.
- ⑥ Bus waiting. Client prepares to send data.