



- ① Writing  $\text{ACKDT} = 0$  specifies sending an  $\overline{\text{ACK}}$ . Writing  $\text{ACKEN} = 1$  initiates a host Acknowledge event. BRG starts. SCLx remains low.
- ② When SCLx is detected low, the module drives SDAx low.
- ③ The BRG times out. Module releases SCLx. BRG restarts.
- ④ BRG times out. Module drives SCLx low, then releases SDAx. Module clears ACKEN. Host generates the interrupt.