



- ① Writing PEN = 1 initiates a host Stop event. BRG starts. Module drives SDAx low.
- ② The BRG times out. Module releases SCLx. BRG restarts.
- ③ The BRG times out. Module releases SDAx. BRG restarts.
- ④ Client logic detects a Stop. Module sets P = 1 and S = 0.
- ⑤ The BRG times out. Module clears PEN. Host generates the interrupt.