



① Client receives address byte, RBF= 1.

② RBF = 1 at ninth clock, clock will be suspended SSPND= 1. Automatic clock stretch begins. Client clears SCLREL bit. Client pulls SCLx line low to stretch clock.

③ User software reads I2CxRCV register. RBF bit clears. Client will stop suspending clock, SSPND= 0. Client sets SCLREL bit to release the clock.

④ Client receives data byte, RBF= 1.

⑤ RBF = 1 at ninth clock, clock will be suspended SSPND= 1. Automatic clock stretch begins. Client clears SCLREL bit. Client pulls SCLx line low to stretch clock.

⑥ User software reads I2CxRCV register. RBF bit clears. Client will stop suspending clock, SSPND= 0. Client sets SCLREL bit to release the clock.