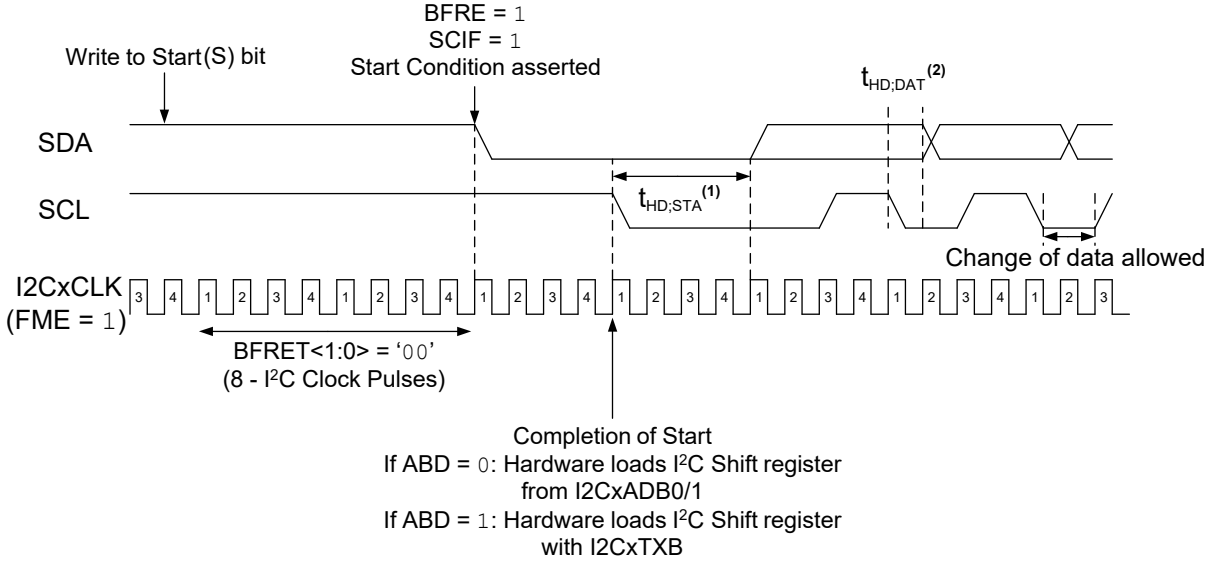
BFRE = 1
SCIF = 1
Start Condition asserted
Write to Start(S) bit
$t_{HD;DAT}$ (2)
SDA
SCL
$t_{HD;STA}$ (1)
Change of data allowed
I2CxCLK
(FME = 1)
BFRET<1:0> = '00'
(8 - I²C Clock Pulses)
Completion of Start
If ABD = 0: Hardware loads I²C Shift register
from I2CxADB0/1
If ABD = 1: Hardware loads I²C Shift register
with I2CxTXB