

**BANK 61**

1E80h	Core Registers
1E8Bh	
1E8Ch	ANSELA
1E8Dh	WPUA
1E8Eh	ODCONA
1E8Fh	SLRCONA
1E90h	INLVLA
1E91h	IOCAP
1E92h	IOCAN
1E93h	IOCAF
1E94h	—
1E95h	—
1E96h	ANSELB <sup>(1)</sup>
1E97h	WPUB <sup>(1)</sup>
1E98h	ODCONB <sup>(1)</sup>
1E99h	SLRCONB <sup>(1)</sup>
1E9Ah	INLVLB <sup>(1)</sup>
1E9Bh	IOCBP <sup>(1)</sup>
1E9Ch	IOCBN <sup>(1)</sup>
1E9Dh	IOCBF <sup>(1)</sup>
1E9Eh	—
1E9Fh	—
1EA0h	ANSELC <sup>(2)</sup>
1EA1h	WPUC <sup>(2)</sup>
1EA2h	ODCONC <sup>(2)</sup>
1EA3h	SLRCONC <sup>(2)</sup>
1EA4h	INLVLC <sup>(2)</sup>
1EA5h	IOCCP <sup>(2)</sup>
1EA6h	IOCCN <sup>(2)</sup>
1EA7h	IOCCF <sup>(2)</sup>

1EA8h	Unimplemented Read as '0'
1EE0h	
1EE1h	RA12C <sup>(4)</sup>
1EE2h	RA22C <sup>(4)</sup>
1EE3h	—
1EE4h	—
1EE5h	RB42C <sup>(1)</sup>
1EE6h	RB52C <sup>(1)</sup>
1EE7h	RB62C <sup>(1)</sup>
1EE8h	RB72C <sup>(1)</sup>
1EE9h	RC02C <sup>(5)</sup>
1EEAh	RC12C <sup>(5)</sup>
1EEBh	—
1EECh	RC42C <sup>(5)</sup>
1EEDh	RC52C <sup>(5)</sup>
1EEFh	—
1EEFh	—
1EF0h	Common RAM (Accesses 70h-7Fh)
1EFFh	

**Note:**

- 1. Available on 20-pin devices only**
- 2. Available on 14/20-pin devices**
- 3. Available on PIC16F181x5 devices only**
- 4. Available on PIC16F18114/15 devices only**
- 5. Available on PIC16F18124/25 devices only**

Legend:



Unimplemented data memory locations, read as '0'