

Bit	7	6	5	4	3	2	1	0
							ADC0REFEN	AC0REFEN
Access							R/W	R/W
Reset							0	0

bit 0 AC0REFEN: AC0 DACREF Reference Force Enable bit

Writing a '1' to this bit forces the voltage reference for AC0 DACREF to be enabled, even if it is not requested.

Writing a '0' to this bit allows to automatic enable/disable of the reference source when not requested.