

| Bit    | 7           | 6   | 5   | 4   | 3        | 2   | 1   | 0   |
|--------|-------------|-----|-----|-----|----------|-----|-----|-----|
|        | IVTADL[7:0] |     |     |     |          |     |     |     |
| Access | R/W         | R/W | R/W | R/W | R/W      | R/W | R/W | R/W |
| Reset  | 0           | 0   | 0   | 0   | <b>1</b> | 0   | 0   | 0   |