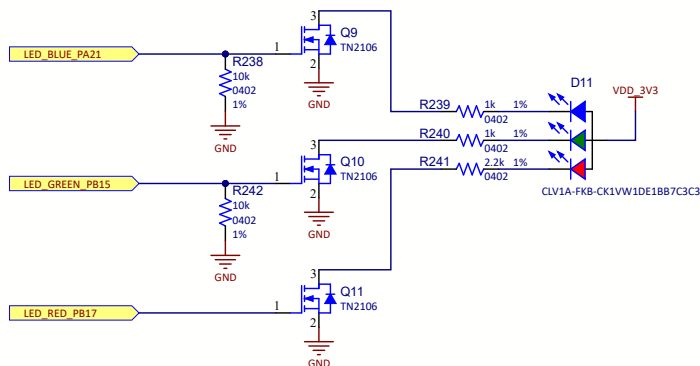
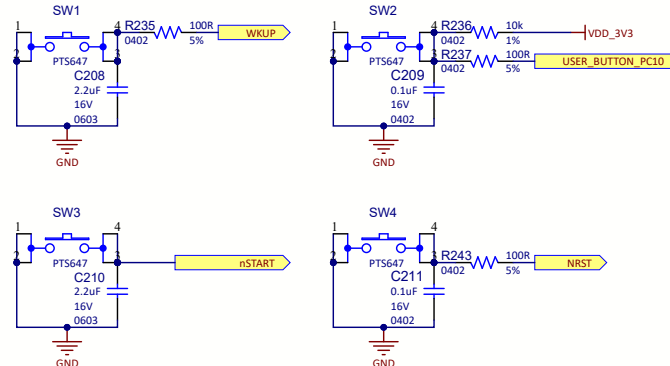


RGB LED



USER Buttons



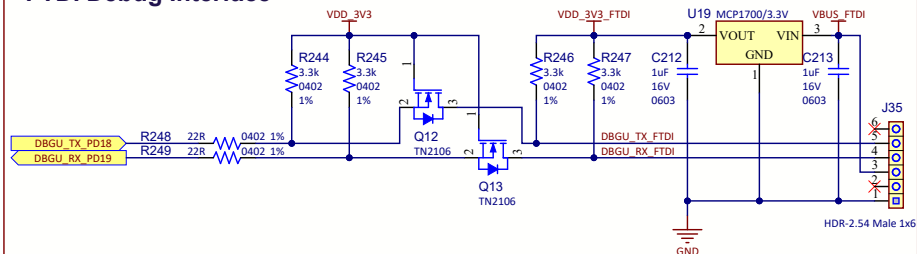
Accessories



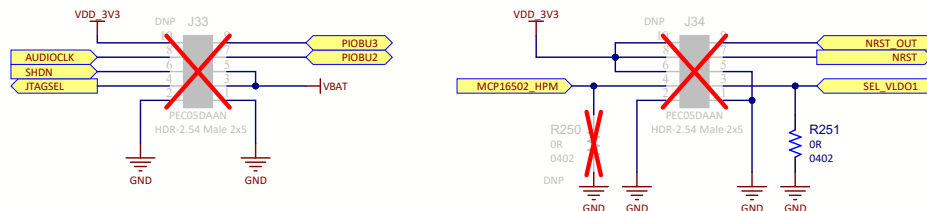
FIDUCIAL ROUND PCB 1mm SMD

- FD1
- FD2
- FD3
- FD4

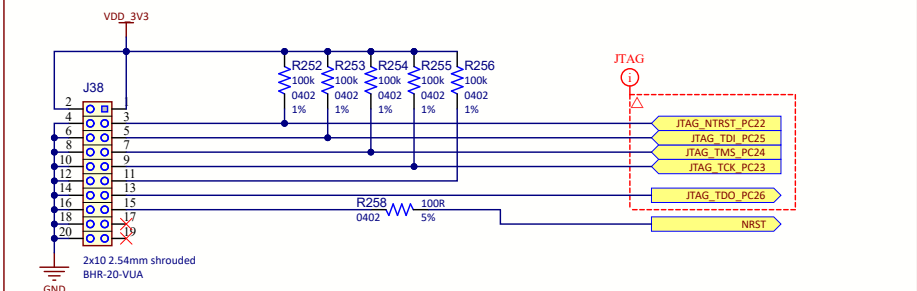
FTDI Debug Interface



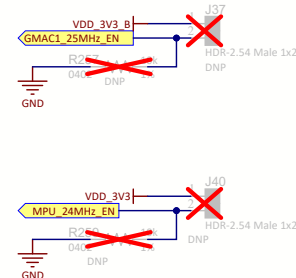
Unused Signals



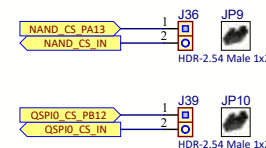
JTAG with SAM-ICE



Clock Enable Management



Memories Chip Select Management



Project Owner: CAV			
PCB Layout Contact: CAV			
PartNumber: 04-12065	Project Title SAMA7D65 Standalone Curiosity	Variant: Standard	
Sheet Title User, Debug Interfaces	SCH #: 02-00995	Rev: 3	
Size B	PCB #: 04-12065	Rev: 2	
File: 15 - LED-User-Debug-SchDoc	Date: 6/26/2024	Sheet 15 of 15	