



- Note
- 1: INTF flag is sampled here (every Q1)
 - 2: Interrupt latency = 3-4 instruction cycles. Latency is the same whether Instruction (PC) is a single cycle or a 2-cycle instruction
 - 3: CLKOUT is available only in RC oscillator mode
 - 4: For minimum width of INT pulse, refer to AC specs
 - 5: INTF is enable to be set anytime during the Q4-Q1 cycles