

○ ADC0

Disable Voltage Pump ☐

Select Timebase Time (in microseconds)

1

Select Operation Mode

Single conversion

Select Prescaler

APB clock divided by 2

Select Sample Length (cycles)

10

**** Conversion Time is 2.16666666667 uS ****

Select Reference

VDD

Select Conversion Trigger

SW Trigger

Select Conversion Start Type

Start a conversion immediately. This bit will be reset to STOP when the co

⊖ Channel Configuration

Enable Differential Mode ☐

Select Positive Input

ADC AIN29 Pin

⊖ Result Configuration

Select Result Resolution

12-bit ADC Result

Enable Result Ready Interrupt



Enable Result Overwrite Interrupt ☐

Enable Result Ready Event Out ☐

Select Result Scaling

The ADC output of the sample or accumulated result is right adjusted

⊕ Sample Configuration

⊕ Window Mode Configuration

⊕ Sleep Mode Configuration