

System

- ⊕ Device & Project Configuration
- ⊕ Cortex-M0+ Configuration
- ⊕ Ports
- ⊕ Clock
- ⊖ Interrupts (NVIC)

Total NVIC Vector Lines

21



NVIC_INT_UPDATE

☐

Vector Number

-15

Number of Interrupts on the
NVIC vector line

1



Enable Reset Vector, invoked

⊕ on Power up and warm reset ☒

Interrupt

Vector Number

-14

Number of Interrupts on the
NVIC vector line

1



Enable Non maskable

⊕ Interrupt, cannot be stopped ☒

or preempted Interrupt

Vector Number

-13

Number of Interrupts on the
NVIC vector line

1

