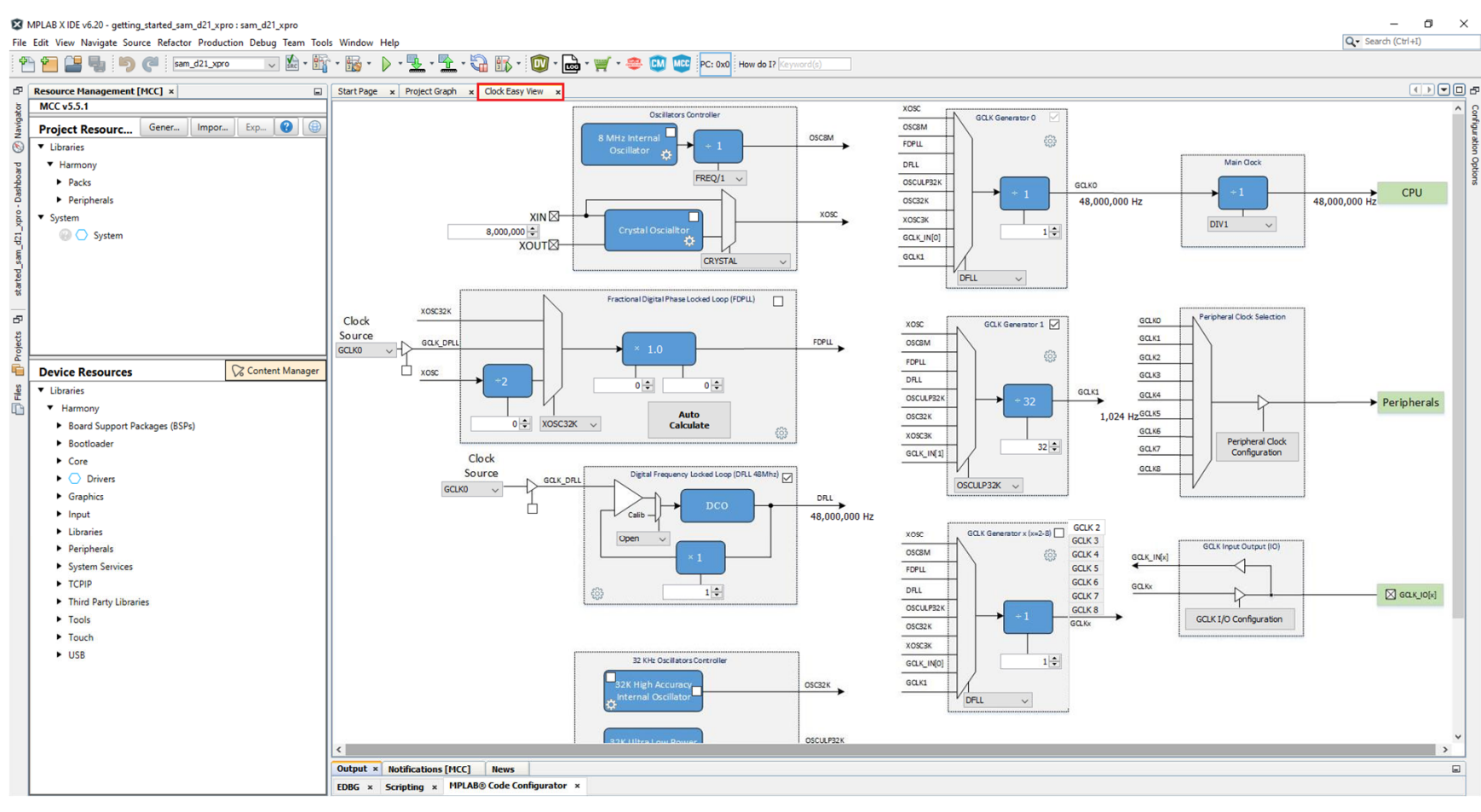
MPLAB X IDE v6.20 - getting_started_sam_d21_xpro : sam_d21_xpro
File Edit View Navigate Source Refactor Production Debug Team Tools Window Help
Resource Management [MCC]
MCC v5.5.1
Project Resourc...
Libraries
Harmony
Packs
Peripherals
System
Device Resources
Content Manager
Start Page
Project Graph
Clock Easy View
Oscillators Controller
8 MHz Internal Oscillator
FREQ/1
OSC8M
XIN
8,000,000
XOUT
Crystal Oscillator
CRYSTAL
XOSC
GCLK Generator 0
DFLL
GCLK0
48,000,000 Hz
Main Clock
DIV1
CPU
Fractional Digital Phase Locked Loop (FDPLL)
Clock Source
GCLK0
XOSC32K
GCLK_DPLL
XOSC
FDPLL
Auto Calculate
GCLK Generator 1
OSCULP32K
GCLK1
1,024 Hz
Peripheral Clock Selection
Peripheral Clock Configuration
Peripherals
Digital Frequency Locked Loop (DFLL 48Mhz)
Calib
Open
DCO
DFLL
48,000,000 Hz
GCLK Generator x (x=2-8)
GCLK Input Output (IO)
GCLK_IN[x]
GCLKx
GCLK_IO[x]
GCLK I/O Configuration
32 KHz Oscillators Controller
32K High Accuracy Internal Oscillator
OSC32K
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