

MCC Modified Filename
D:\Test_Projects\Forum\USART_SPI_HOST_DMA\USART_SPI_HOST_DMA.X\src\config\default\peripheral\usart\plb_usart0.c

Graphical Textual

MCC Updated Code : Generated	1/2	Merge Result : plb_usart0.c
<pre>static void USART0_ErrorClear(void) { uint32_t dummyData = 0U; if ((USART0_REGS->US_CSR & (US_CSR_USART_OVRE_Msk US_CSR_USART_PARE_Msk US_CSR_USART_FE_Msk)) != 0U) { /* Clear the error flags */ USART0_REGS->US_CR = US_CR_USART_RSTSTA_Msk; /* Flush existing error bytes from the RX FIFO */ while ((USART0_REGS->US_CSR & US_CSR_USART_RXRDY_Msk) != 0U) { dummyData = USART0_REGS->US_RHR & US_RHR_RXCHR_Msk; } /* Ignore the warning */ (void) dummyData; } } volatile static USART_OBJECT usart0Obj; static void __attribute__((used)) USART0_ISR_RX_Handler(void) { uint32_t rxData = 0U; uint8_t* pu8Data = (uint8_t *)usart0Obj.rxBuffer; uint16_t* pul6Data = (uint16_t *)usart0Obj.rxBuffer; if(usart0Obj.rxBusyStatus == true) { size_t rxSize = usart0Obj.rxSize; size_t rxProcessedSize = usart0Obj.rxProcessedSize; while(((USART0_REGS->US_CSR & US_CSR_USART_RXRDY_Msk) != 0U) && (rxSize > rxProcessedSize)) { rxData = (USART0_REGS->US_RHR & US_RHR_RXCHR_Msk); if ((USART0_REGS->US_MR & US_MR_USART_MODE9_Msk) != 0U) { pul6Data[rxProcessedSize] = (uint16_t) rxData; } } } }</pre>	<pre>51 51 static void USART0_ErrorClear(void) 52 52 { 53 53 uint32_t dummyData = 0U; 54 54 55 55 if ((USART0_REGS->US_CSR & (US_CSR_USART_OVRE_Msk US_CSR_USART_PARE_Msk US_CSR_USART_FE_Msk)) != 0U) 56 56 { 57 57 /* Clear the error flags */ 58 58 USART0_REGS->US_CR = US_CR_USART_RSTSTA_Msk; 59 59 60 60 /* Flush existing error bytes from the RX FIFO */ 61 61 while ((USART0_REGS->US_CSR & US_CSR_USART_RXRDY_Msk) != 0U) 62 62 { 63 63 dummyData = USART0_REGS->US_RHR & US_RHR_RXCHR_Msk; 64 64 } 65 65 66 66 /* Ignore the warning */ 67 67 (void) dummyData; 68 68 69 69 } 70 70 71 71 static void custom_config(void) 72 72 { 73 73 // Custom Config 74 74 } 75 75</pre>	<pre>static void USART0_ErrorClear(void) { uint32_t dummyData = 0U; if ((USART0_REGS->US_CSR & (US_CSR_USART_OVRE_Msk US_CSR_USART_PARE_Msk US_CSR_USART_FE_Msk)) != 0U) { /* Clear the error flags */ USART0_REGS->US_CR = US_CR_USART_RSTSTA_Msk; /* Flush existing error bytes from the RX FIFO */ while ((USART0_REGS->US_CSR & US_CSR_USART_RXRDY_Msk) != 0U) { dummyData = USART0_REGS->US_RHR & US_RHR_RXCHR_Msk; } /* Ignore the warning */ (void) dummyData; } } volatile static USART_OBJECT usart0Obj; static void __attribute__((used)) USART0_ISR_RX_Handler(void) { uint32_t rxData = 0U; uint8_t* pu8Data = (uint8_t *)usart0Obj.rxBuffer; uint16_t* pul6Data = (uint16_t *)usart0Obj.rxBuffer; if(usart0Obj.rxBusyStatus == true) { size_t rxSize = usart0Obj.rxSize; size_t rxProcessedSize = usart0Obj.rxProcessedSize; while(((USART0_REGS->US_CSR & US_CSR_USART_RXRDY_Msk) != 0U) && (rxSize > rxProcessedSize)) { rxData = (USART0_REGS->US_RHR & US_RHR_RXCHR_Msk); if ((USART0_REGS->US_MR & US_MR_USART_MODE9_Msk) != 0U) { pul6Data[rxProcessedSize] = (uint16_t) rxData; } } } }</pre>