



Timer Setup:

START = None (ON = 1)
 CSYNC = Async

RESET = None
 RDSEL = TUxyTMR access

STOP = At PR Match

Note:

1. TUxyTMR is not double buffered and software access is not guarded for clock collisions.
2. Capture and CIF interrupt occur only with an additional input clock after pulse output is asserted.
3. The effect of CLR command is immediate when CSYNC = 0 and ON = 0.
4. ZIF interrupt does not occur because timer is not running (ON = 0).
5. Cleared by software.