

Channel x
selected clock



CHIDx(PWM_SR)



CHIDx(PWM_ENA)



CHIDx(PWM_DIS)



PWM_CCNTx



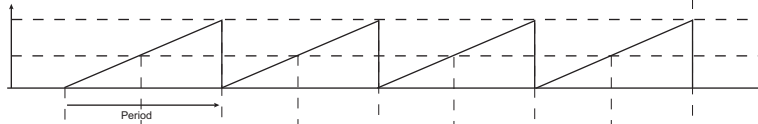
CPRDx(PWM_CPRDx)



CDTYx(PWM_CDTYx)



Left Aligned
CALG(PWM_CMRx) = 0



Output Waveforms

