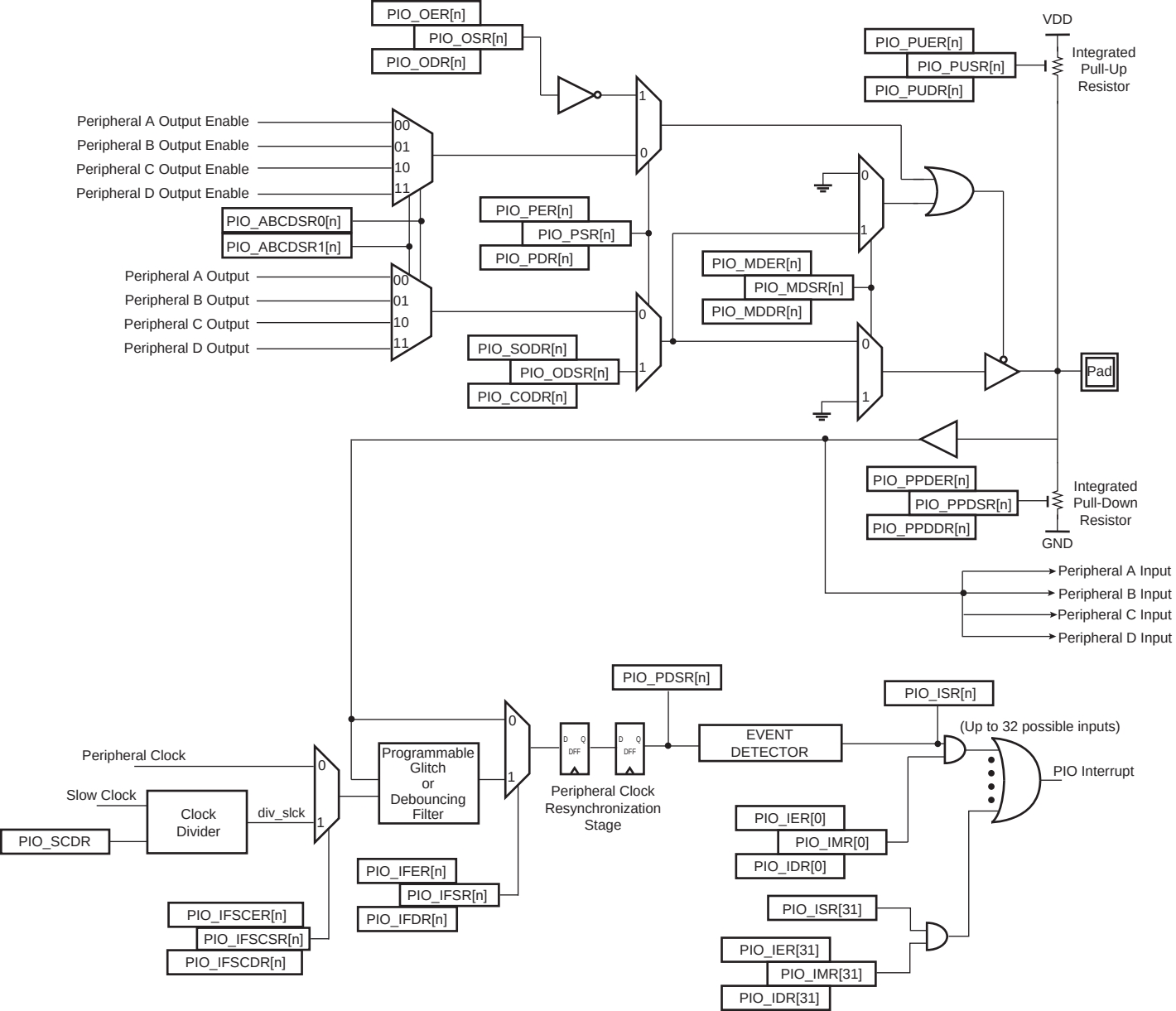
PIO_OER[n]
PIO_OSR[n]
PIO_ODR[n]
VDD
PIO_PUER[n]
PIO_PUSR[n]
PIO_PUDR[n]
Integrated Pull-Up Resistor
Peripheral A Output Enable
Peripheral B Output Enable
Peripheral C Output Enable
Peripheral D Output Enable
00
01
10
11
PIO_ABCDSR0[n]
PIO_ABCDSR1[n]
PIO_PER[n]
PIO_PSR[n]
PIO_PDR[n]
PIO_MDER[n]
PIO_MDSR[n]
PIO_MDDR[n]
Peripheral A Output
Peripheral B Output
Peripheral C Output
Peripheral D Output
PIO_SODR[n]
PIO_ODSR[n]
PIO_CODR[n]
Pad
PIO_PPDER[n]
PIO_PPDSR[n]
PIO_PPDDR[n]
Integrated Pull-Down Resistor
GND
Peripheral A Input
Peripheral B Input
Peripheral C Input
Peripheral D Input
PIO_PDSR[n]
PIO_ISR[n]
(Up to 32 possible inputs)
Peripheral Clock
Slow Clock
Clock Divider
div_slck
PIO_SCDR
Programmable Glitch or Debouncing Filter
D Q DFF
Peripheral Clock Resynchronization Stage
EVENT DETECTOR
PIO Interrupt
PIO_IER[0]
PIO_IMR[0]
PIO_IDR[0]
PIO_IFER[n]
PIO_IFSR[n]
PIO_IFDR[n]
PIO_IFSCER[n]
PIO_IFSCSR[n]
PIO_IFSCDR[n]
PIO_ISR[31]
PIO_IER[31]
PIO_IMR[31]
PIO_IDR[31]