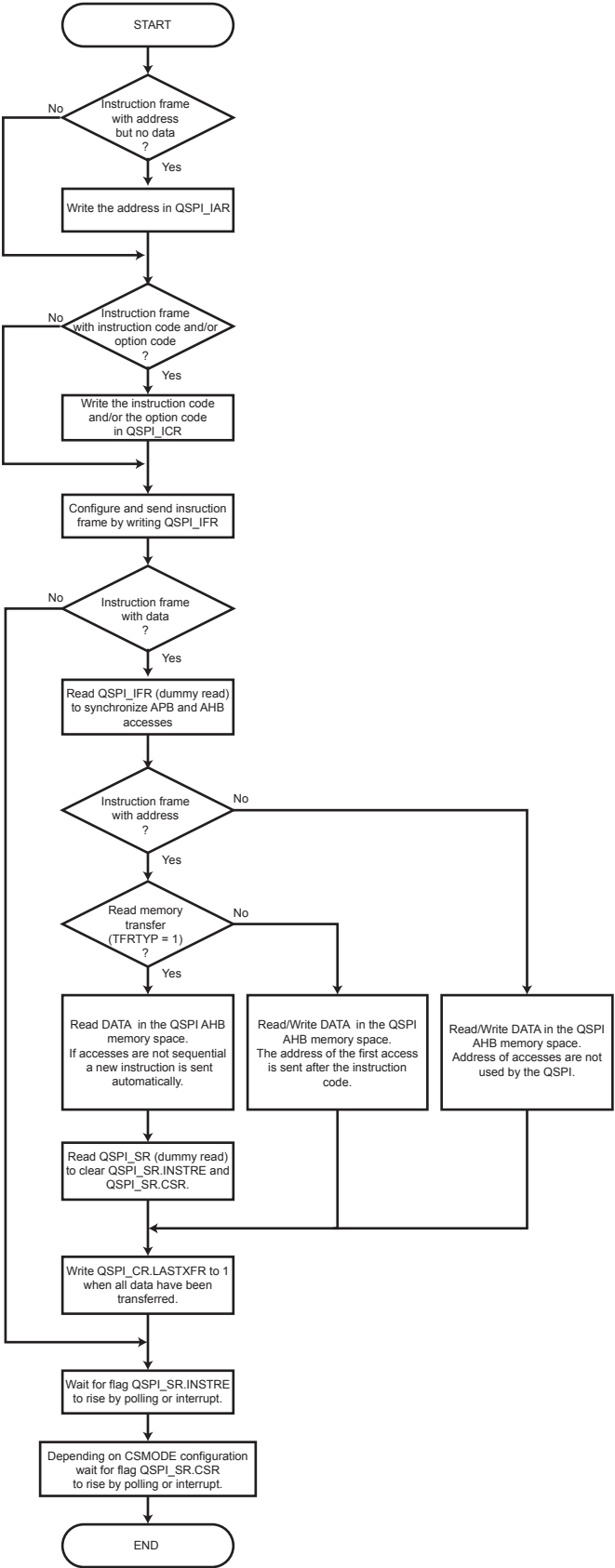
START
Instruction frame with address but no data ?
No
Yes
Write the address in QSPI_IAR
Instruction frame with instruction code and/or option code ?
No
Yes
Write the instruction code and/or the option code in QSPI_ICR
Configure and send insruction frame by writing QSPI_IFR
Instruction frame with data ?
No
Yes
Read QSPI_IFR (dummy read) to synchronize APB and AHB accesses
Instruction frame with address ?
No
Yes
Read memory transfer (TFRTYP = 1) ?
No
Yes
Read DATA in the QSPI AHB memory space. If accesses are not sequential a new instruction is sent automatically.
Read/Write DATA in the QSPI AHB memory space. The address of the first access is sent after the instruction code.
Read/Write DATA in the QSPI AHB memory space. Address of accesses are not used by the QSPI.
Read QSPI_SR (dummy read) to clear QSPI_SR.INSTRE and QSPI_SR.CSR.
Write QSPI_CR.LASTXFR to 1 when all data have been transferred.
Wait for flag QSPI_SR.INSTRE to rise by polling or interrupt.
Depending on CSMODE configuration wait for flag QSPI_SR.CSR to rise by polling or interrupt.
END