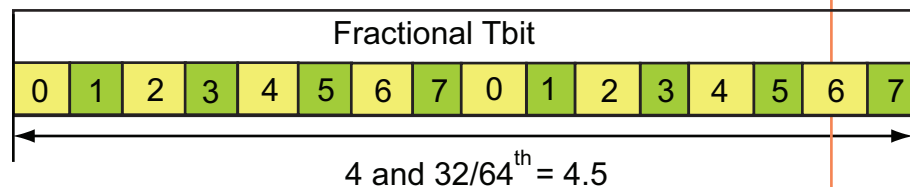
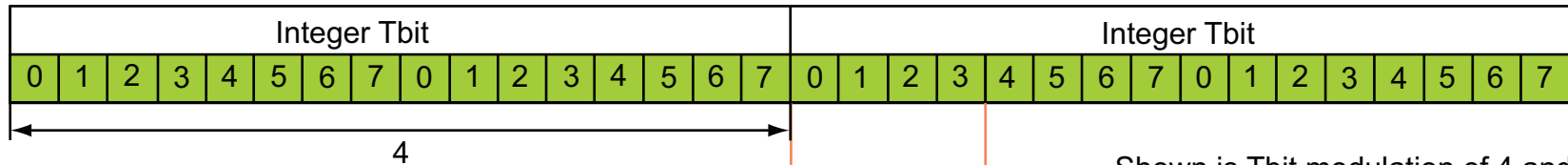


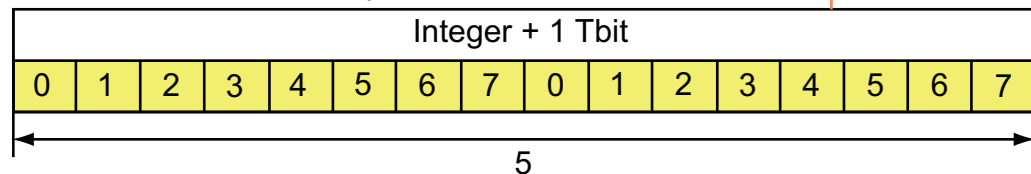
# Bit Rate Modulation with Tbit Times


BR = 4 = 64 CCC per count




Shown is Tbit modulation of 4 and 5 integer divisor value bit rates to achieve an average bit rate of 4 and 1/2 or 4.5.

BR = 5 = 16x5 = 80 CCC per count



 BR = DMR+DLR

 BR = DMR+DLR+1