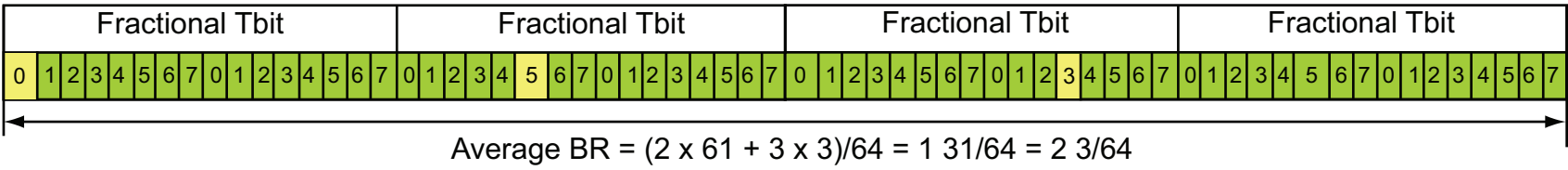
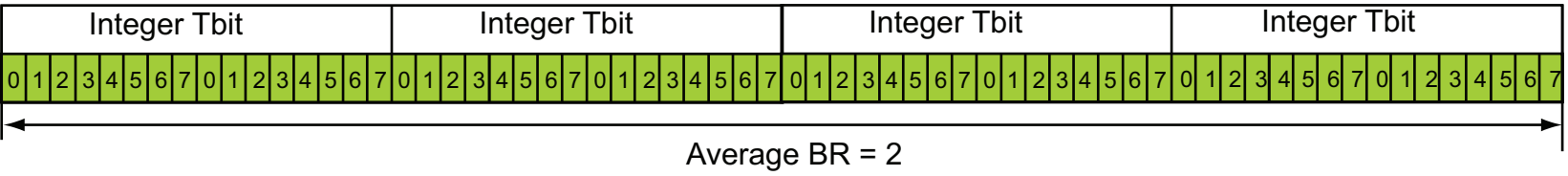


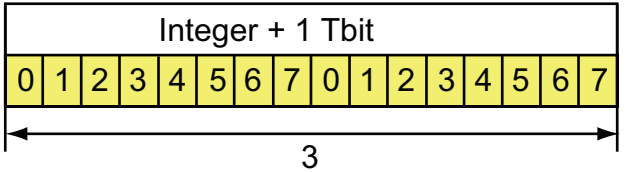
Bit Rate Modulation with Tbit Times

BR = 2 = 32 CCC per count



th

BR = 3 = 16x3 = 48 CCC per count



 BR = DMR+DLR

 BR = DMR+DLR+1