

SPI Host:
Microcontroller

Data Clock (SCK)

Data Output (SO)

Data Input (SI)

$\overline{CS3}$ $\overline{CS2}$ $\overline{CS1}$ $\overline{CS0}$

SI SO SCK

Client 0
AT25XXX

\overline{CS}

SI SO SCK

Client 1
AT25XXX

\overline{CS}

SI SO SCK

Client 2
AT25XXX

\overline{CS}

SI SO SCK

Client 3
AT25XXX

\overline{CS}

