AT25512: Package Marking Information

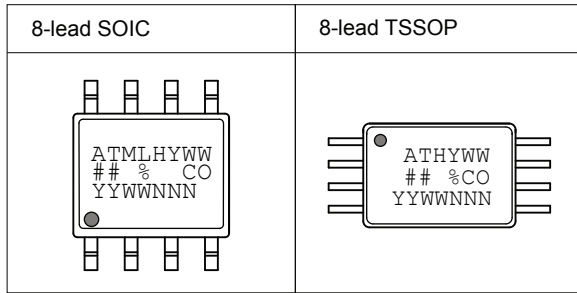| 8-lead SOIC	8-lead TSSOP
ATMLHYWW ## % CO YYWWNNN	ATHYWW ## %CO YYWWNNN

Note 1: ● designates pin 1
Note 2: Package drawings are not to scale

Catalog Number Truncation

AT25512   Truncation Code ##: 5F

Date Codes				Voltages	
YY = Year		**Y = Year**	**WW = Work Week of Assembly**	**% = Minimum Voltage**	
16: 2016 20: 2020		6: 2016 0: 2020	02: Week 2	L or M: 1.8V min	
17: 2017 21: 2021		7: 2017 1: 2021	04: Week 4		
18: 2018 22: 2022		8: 2018 2: 2022	...		
19: 2019 23: 2023		9: 2019 3: 2023	52: Week 52		

Country of Origin	Device Grade	Atmel Truncation
CO = Country of Origin	H or U: Industrial Grade	AT: Atmel ATM: Atmel ATML: Atmel

Lot Number or Trace Code

NNN = Alphanumeric Trace Code (2 Characters for Small Packages)