

Bit	7	6	5	4	3	2	1	0
	RUNSTDBY				OUTMODE[1:0]		EVENTEN	ALWAYSON
Access	R/W				R/W	R/W	R/W	R/W
Reset	0				0	0	0	0

Bit 7 – RUNSTDBY Run in Standby Mode

This bit controls whether or not the op amp functions in Standby sleep mode.

Value	Description
0	OPn is disabled when in Standby sleep mode, and its output driver is disabled.
1	OPn will continue operating as configured in Standby sleep mode.

Bits 3:2 – OUTMODE[1:0] Output Mode

This bit field selects the output mode of the output driver.

Value	Name	Description
0x0	OFF	The output driver for OPn is disabled, but this can be overridden by the DRIVEN event.
0x1	NORMAL	The output driver for OPn is enabled in Normal mode.
0x2 – 0x3	-	Reserved

Bit 1 – EVENTEN Event Enable

This bit enables event reception and generation.

Value	Description
0	No events are enabled for OPn.
1	All events are enabled for OPn.

Bit 0 – ALWAYSON Always On

This bit controls whether the op amp is always on or not.

Value	Description
0	OPn is not always on, but can be enabled by the ENABLEN event and disabled by the DISABLEN event.
1	OPn is always on.