


	BANK 56	BANK 57	BANK 58	BANK 59	BANK 60	BANK 61	BANK 62	BANK 63
1C00h	Core Registers	Core Registers	Core Registers	Core Registers	Core Registers	Core Registers	Core Registers	Core Registers
1C0Bh	Unimplemented Read as '0'	Unimplemented Read as '0'	Unimplemented Read as '0'	Unimplemented Read as '0'	UMTOAP	See Table 2 for register mapping details	See Table 3 for register mapping details	Unimplemented Read as '0'
1C0Ch					UMTOAL			
					UMTOAH			
					Unimplemented Read as '0'			
1C6Fh	Common RAM (Accesses 70h-7Fh)	Common RAM (Accesses 70h-7Fh)	Common RAM (Accesses 70h-7Fh)	Common RAM (Accesses 70h-7Fh)	Common RAM (Accesses 70h-7Fh)	Common RAM (Accesses 70h-7Fh)	Common RAM (Accesses 70h-7Fh)	Common RAM (Accesses 70h-7Fh)
1C70h								
1C7Fh								

Legend:

 Unimplemented data memory locations, read as '0'