

BANK 62

1F00h	Core registers	1F3Bh	SLRCONA
1F0Bh		1F3Ch	INLVLA
1F0Ch	Unimplemented Read as '0'	1F3Dh	IOCAP
1F0Fh		1F3Eh	IOCAN
1F10h	RA0PPS	1F3Fh	IOCAF
1F11h	RA1PPS	1F40h	Unimplemented Read as '0'
1F12h	RA2PPS	1F42h	
1F13h	RA3PPS	1F43h	WPUB ⁽¹⁾
1F14h	RA4PPS	1F44h	ODCONB ⁽¹⁾
1F15h	RA5PPS	1F45h	SLRCONB ⁽¹⁾
1F16h	Unimplemented Read as '0'	1F46h	INLVLB ⁽¹⁾
1F1Bh		1F47h	IOCBP ⁽¹⁾
1F1Ch	RB4PPS ⁽¹⁾	1F48h	IOCBN ⁽¹⁾
1F1Dh	RB5PPS ⁽¹⁾	1F49h	IOCBF ⁽¹⁾
1F1Eh	RB6PPS ⁽¹⁾	1F4Ah	Unimplemented Read as '0'
1F1Fh	RB7PPS ⁽¹⁾	1F4Bh	
1F20h	RC0PPS	1F4Dh	WPUA
1F21h	RC1PPS	1F4Eh	ODCONC
1F22h	RC2PPS	1F4Fh	SLRCONC
1F23h	RC3PPS	1F50h	INLVLC
1F24h	RC4PPS	1F51h	IOCCP
1F25h	RC5PPS	1F52h	IOCCN
1F26h	RC6PPS ⁽¹⁾	1F53h	IOCCF
1F27h	RC7PPS ⁽¹⁾	1F54h	Unimplemented Read as '0'
1F28h	Unimplemented Read as '0'	1F55h	
1F37h		ANSELA	1F56h
1F38h	WPUA	1F6Fh	
1F39h	ODCONA	1F70h	
1F3Ah		1F7Fh	

Note: 1. PIC16F15245 only

Legend:



Unimplemented data memory locations, read as '0'