

BANK 61

1E80h	Core Registers
1E8Bh	
1E8Ch	ANSELA
1E8Dh	WPUA
1E8Eh	ODCONA
1E8Fh	SLRCONA
1E90h	INLVLA
1E91h	IOCAP
1E92h	IOCAN
1E93h	IOCAF
1E94h	—
1E95h	—
1E96h	ANSELB ⁽¹⁾
1E97h	WPUB ⁽¹⁾
1E98h	ODCONB ⁽¹⁾
1E99h	SLRCONB ⁽¹⁾
1E9Ah	INLVLB ⁽¹⁾
1E9Bh	IOCBP ⁽¹⁾
1E9Ch	IOCBN ⁽¹⁾
1E9Dh	IOCBF ⁽¹⁾
1E9Eh	—
1E9Fh	—
1EA0h	ANSELC ⁽²⁾
1EA1h	WPUC ⁽²⁾
1EA2h	ODCONC ⁽²⁾
1EA3h	SLRCONC ⁽²⁾
1EA4h	INLVLC ⁽²⁾
1EA5h	IOCCP ⁽²⁾
1EA6h	IOCCN ⁽²⁾
1EA7h	IOCCF ⁽²⁾

1EA8h	Unimplemented Read as '0'
1EE0h	
1EE1h	RA12C ⁽⁴⁾
1EE2h	RA22C ⁽⁴⁾
1EE3h	—
1EE4h	—
1EE5h	RB42C ⁽¹⁾
1EE6h	RB52C ⁽¹⁾
1EE7h	RB62C ⁽¹⁾
1EE8h	RB72C ⁽¹⁾
1EE9h	RC02C ⁽⁵⁾
1EEAh	RC12C ⁽⁵⁾
1EEBh	—
1EECh	RC42C ⁽⁵⁾
1EEDh	RC52C ⁽⁵⁾
1EEFh	—
1EEFh	—
1EF0h	Common RAM (Accesses 70h-7Fh)
1EFFh	

Note:

- 1. Available on 20-pin devices only**
- 2. Available on 14/20-pin devices**
- 3. Available on PIC16F171x5 devices only**
- 4. Available on PIC16F17114/15 devices only**
- 5. Available on PIC16F17124/25 devices only**

Legend:



Unimplemented data memory locations, read as '0'