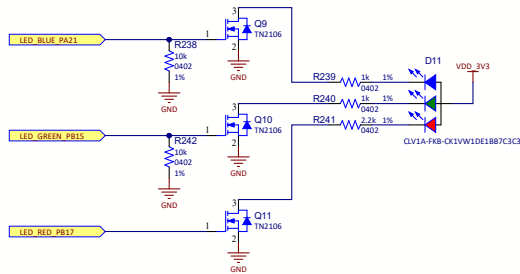
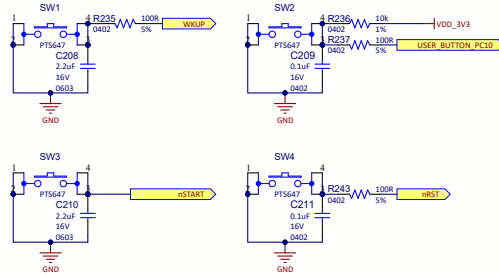


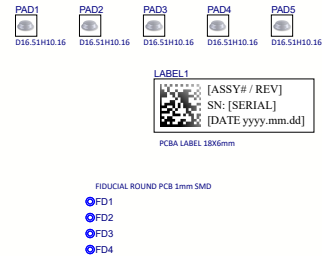
RGB LED



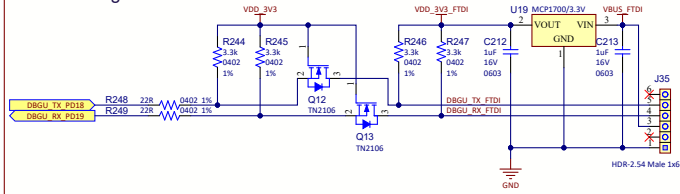
USER Buttons



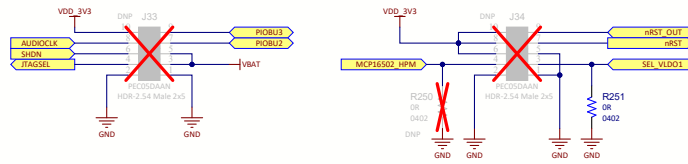
Accessories



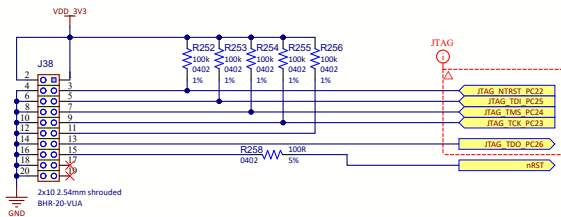
FTDI Debug Interface



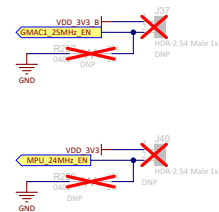
Unused Signals



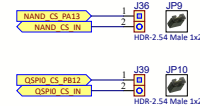
JTAG with SAM-ICE



Clock Enable Management



Memories Chip Select Management



Project Owner:	CAV
PCB Layout Contact:	CAV

PartNumber:	Project Title	Variant: Standard
04-12169	SAMA7D65 Curiosity	

User, Debug Interfaces

Variant: **Standard**

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