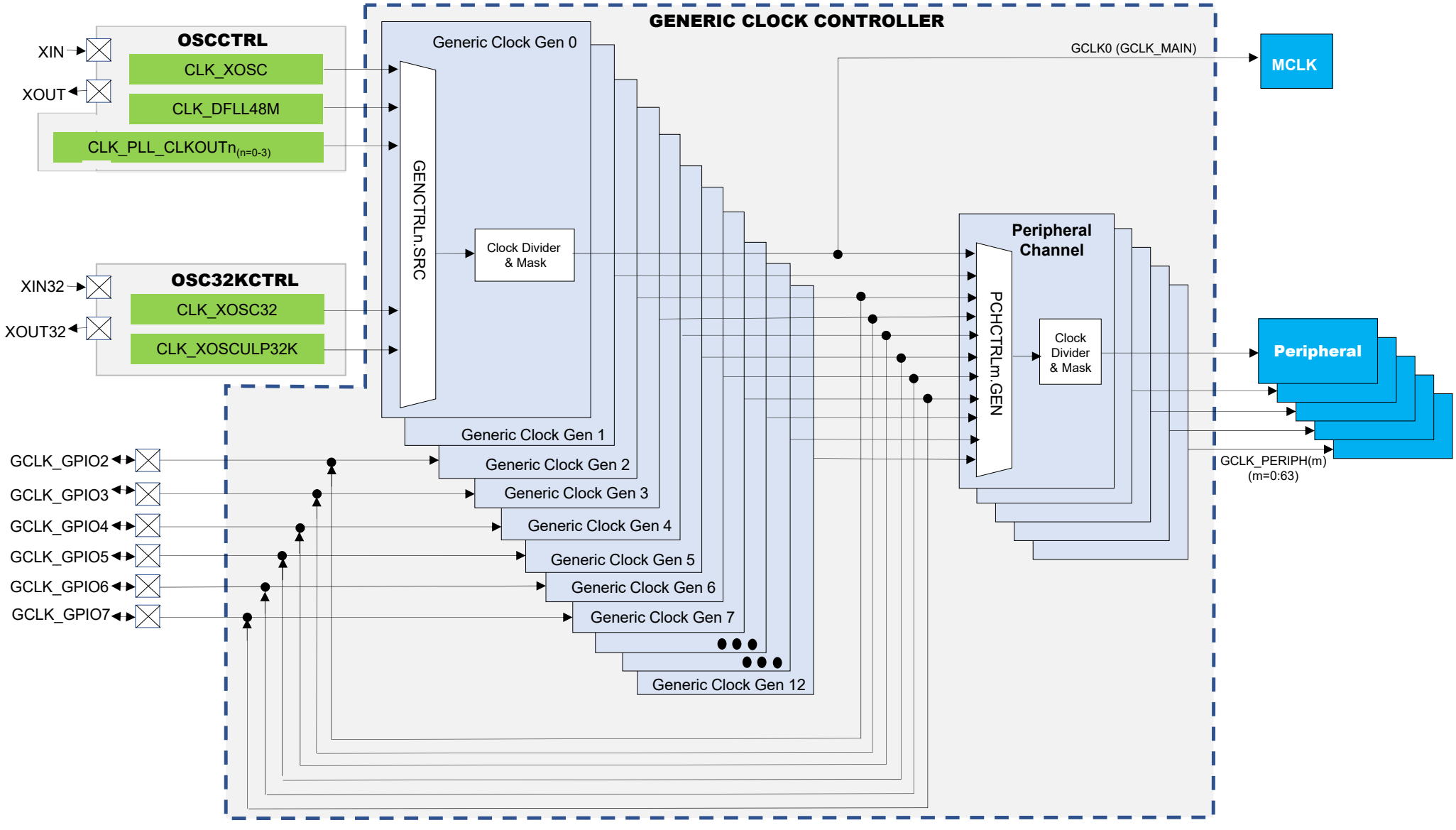
GENERIC CLOCK CONTROLLER
OSCCTRL
XIN
XOUT
CLK_XOSC
CLK_DFLL48M
CLK_PLL_CLKOUTn(n=0-3)
OSC32KCTRL
XIN32
XOUT32
CLK_XOSC32
CLK_XOSCULP32K
GENCTRLn.SRC
Generic Clock Gen 0
Clock Divider & Mask
Generic Clock Gen 1
Generic Clock Gen 2
Generic Clock Gen 3
Generic Clock Gen 4
Generic Clock Gen 5
Generic Clock Gen 6
Generic Clock Gen 7
Generic Clock Gen 12
GCLK_GPIO2
GCLK_GPIO3
GCLK_GPIO4
GCLK_GPIO5
GCLK_GPIO6
GCLK_GPIO7
GCLK0 (GCLK_MAIN)
MCLK
Peripheral Channel
PCHCTRLm.GEN
Clock Divider & Mask
Peripheral
GCLK_PERIPH(m)
(m=0:63)