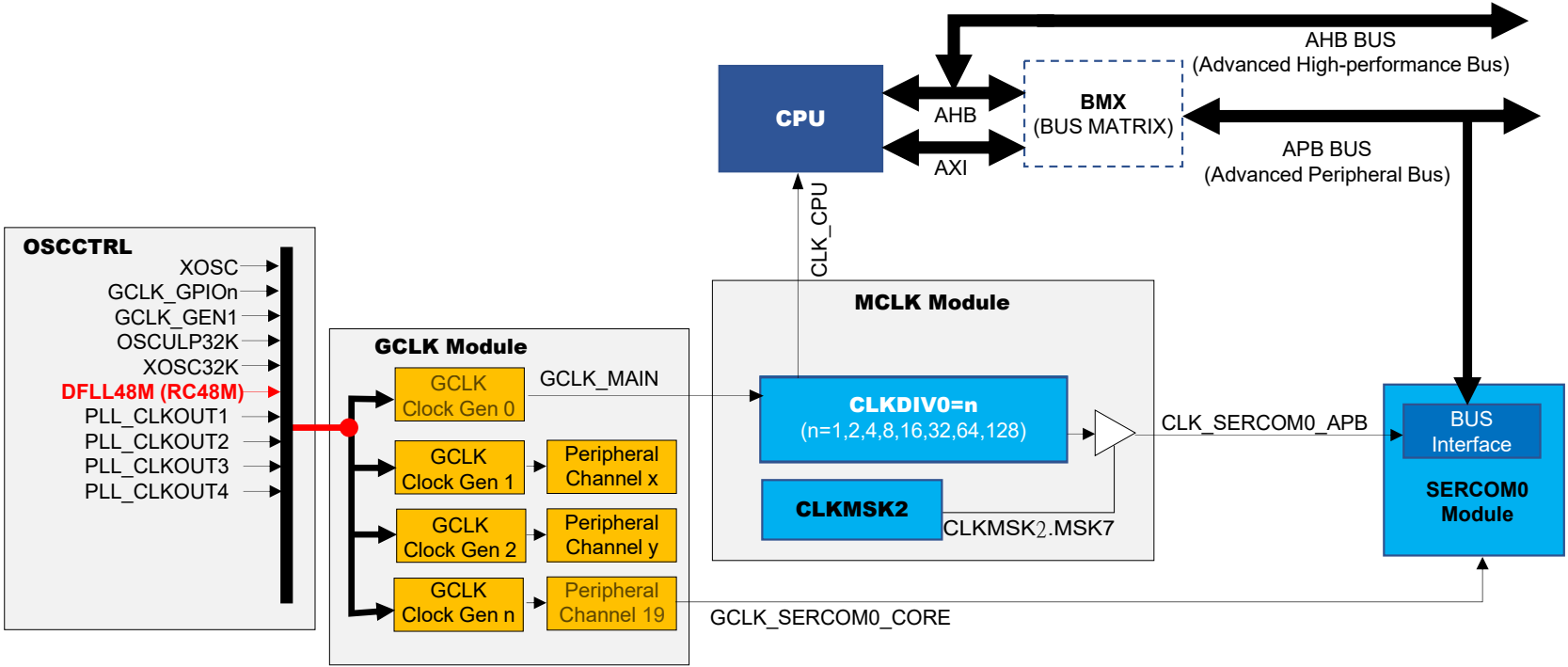
AHB BUS
(Advanced High-performance Bus)
CPU
AHB
AXI
BMX
(BUS MATRIX)
APB BUS
(Advanced Peripheral Bus)
CLK_CPU
OSCCTRL
XOSC
GCLK_GPIOn
GCLK_GEN1
OSCULP32K
XOSC32K
DFLL48M (RC48M)
PLL_CLKOUT1
PLL_CLKOUT2
PLL_CLKOUT3
PLL_CLKOUT4
GCLK Module
GCLK
Clock Gen 0
GCLK_MAIN
GCLK
Clock Gen 1
Peripheral
Channel x
GCLK
Clock Gen 2
Peripheral
Channel y
GCLK
Clock Gen n
Peripheral
Channel 19
MCLK Module
CLKDIV0=n
(n=1,2,4,8,16,32,64,128)
CLKMSK2
CLKMSK2.MSK7
CLK_SERCOM0_APB
BUS
Interface
SERCOM0
Module
GCLK_SERCOM0_CORE