

BANK 32		BANK 33		BANK 34		BANK 35		BANK 36		BANK 37		BANK 38		BANK 39	
1000h	Core Registers	1080h	Core Registers	1100h	Core Registers	1180h	Core Registers	1200h	Core Registers	1280h	Core Registers	1300h	Core Registers	1380h	Core Registers
100Bh		108Bh		110Bh		118Bh		120Bh		128Bh		130Bh		138Bh	
100Ch	—	108Ch	—	110Ch	—	118Ch	—	120Ch	—	128Ch	—	130Ch	—	138Ch	—
100Dh	—	108Dh	—	110Dh	—	118Dh	—	120Dh	—	128Dh	—	130Dh	—	138Dh	—
100Eh	—	108Eh	—	110Eh	—	118Eh	—	120Eh	—	128Eh	—	130Eh	—	138Eh	—
100Fh	—	108Fh	—	110Fh	—	118Fh	—	120Fh	—	128Fh	—	130Fh	—	138Fh	—
1010h	—	1090h	—	1110h	—	1190h	—	1210h	—	1290h	—	1310h	—	1390h	—
1011h	—	1091h	—	1111h	—	1191h	—	1211h	—	1291h	—	1311h	—	1391h	—
1012h	—	1092h	—	1112h	—	1192h	—	1212h	—	1292h	—	1312h	—	1392h	—
1013h	—	1093h	—	1113h	—	1193h	—	1213h	—	1293h	—	1313h	—	1393h	—
1014h	—	1094h	—	1114h	—	1194h	—	1214h	—	1294h	—	1314h	—	1394h	—
1015h	—	1095h	—	1115h	—	1195h	—	1215h	—	1295h	—	1315h	—	1395h	—
1016h	—	1096h	—	1116h	—	1196h	—	1216h	—	1296h	—	1316h	—	1396h	—
1017h	—	1097h	—	1117h	—	1197h	—	1217h	—	1297h	—	1317h	—	1397h	—
1018h	—	1098h	—	1118h	—	1198h	—	1218h	—	1298h	—	1318h	—	1398h	—
1019h	—	1099h	—	1119h	—	1199h	—	1219h	—	1299h	—	1319h	—	1399h	—
101Ah	—	109Ah	—	111Ah	—	119Ah	—	121Ah	—	129Ah	—	131Ah	—	139Ah	—
101Bh	—	109Bh	—	111Bh	—	119Bh	—	121Bh	—	129Bh	—	131Bh	—	139Bh	—
101Ch	—	109Ch	—	111Ch	—	119Ch	—	121Ch	—	129Ch	—	131Ch	—	139Ch	—
101Dh	—	109Dh	—	111Dh	—	119Dh	—	121Dh	—	129Dh	—	131Dh	—	139Dh	—
101Eh	—	109Eh	—	111Eh	—	119Eh	—	121Eh	—	129Eh	—	131Eh	—	139Eh	—
101Fh	—	109Fh	—	111Fh	—	119Fh	—	121Fh	—	129Fh	—	131Fh	—	139Fh	—
1020h	Unimplemented Read as '0'	10A0h	Unimplemented Read as '0'	1120h	Unimplemented Read as '0'	11A0h	Unimplemented Read as '0'	1220h	Unimplemented Read as '0'	12A0h	Unimplemented Read as '0'	1320h	Unimplemented Read as '0'	13A0h	Unimplemented Read as '0'
106Fh		10EFh		116Fh		11EFh		126Fh		12EFh		136Fh		13EFh	
1070h		10F0h		1170h		11F0h		1270h		12F0h		1370h		13F0h	
107Fh		10FFh		117Fh		11FFh		127Fh		12FFh		137Fh		13FFh	
	Common RAM (Accesses 70h-7Fh)		Common RAM (Accesses 70h-7Fh)		Common RAM (Accesses 70h-7Fh)		Common RAM (Accesses 70h-7Fh)		Common RAM (Accesses 70h-7Fh)		Common RAM (Accesses 70h-7Fh)		Common RAM (Accesses 70h-7Fh)		Common RAM (Accesses 70h-7Fh)

Legend:

Unimplemented data memory locations, read as '0'