

```
// SPDX-License-Identifier: (GPL-2.0+ OR MIT)
/*
 * sam9x60.dtsi - Device Tree Include file for Microchip SAM9X60 SoC
 *
 * Copyright (C) 2019 Microchip Technology Inc. and its subsidiaries
 *
 * Author: Sandeep Sheriker M <sandeepsheriker.mallikarjun@microchip.com>
 */
```

```
#include <dt-bindings/dma/at91.h>
#include <dt-bindings/pinctrl/at91.h>
#include <dt-bindings/interrupt-controller/irq.h>
#include <dt-bindings/gpio/gpio.h>
#include <dt-bindings/clock/at91.h>
#include <dt-bindings/mfd/at91-usart.h>
#include <dt-bindings/mfd/atmel-flexcom.h>
```

```
{
    #address-cells = <1>;
    #size-cells = <1>;
    model = "Microchip SAM9X60 SoC";
    compatible = "microchip,sam9x60";
    interrupt-parent = <&aic>;
```

```
aliases {
    serial0 = &dbg;
    gpio0 = &pioA;
    gpio1 = &pioB;
    gpio2 = &pioC;
    gpio3 = &pioD;
    tcb0 = &tcb0;
    tcb1 = &tcb1;
};
```



```
// SPDX-License-Identifier: (GPL-2.0+ OR MIT)
/*
 * at91-sam9x60-curiosity.dts - Device Tree file for Microchip SAM9X60 Curiosity board
 *
 * Copyright (C) 2022 Microchip Technology Inc. and its subsidiaries
 *
 * Author: Durai Manickam KR <durai.manickamkr@microchip.com>
 */
```

```
/dts-v1/;
#include "sam9x60.dtsi"
#include <dt-bindings/input/input.h>
```

```
{
    model = "Microchip SAM9X60 Curiosity";
    compatible = "microchip,sam9x60-curiosity", "microchip,sam9x60", "atmel,at91sam9";

    aliases {
        i2c0 = &i2c0;
        i2c1 = &i2c6;
        serial2 = &uart7;
    };

    chosen {
        stdout-path = "serial0:115200n8";
    };

    memory@20000000 {
        reg = <0x20000000 0x8000000>;
    };
};
```

```
/dts-v1/;
{
    #address-cells = <0x01>;
    #size-cells = <0x01>;
    model = "Microchip SAM9X60 Curiosity";
    compatible = "microchip,sam9x60-curiosity", "microchip,sam9x60", "atmel,at91sam9";
    interrupt-parent = <0x01>;

    aliases {
        serial0 = "/ahb/apb/serial@ffff200";
        gpio0 = "/ahb/apb/pinctrl@ffff400/gpio@ffff400";
        gpio1 = "/ahb/apb/pinctrl@ffff400/gpio@ffff600";
        gpio2 = "/ahb/apb/pinctrl@ffff400/gpio@ffff800";
        gpio3 = "/ahb/apb/pinctrl@ffff400/gpio@ffffa00";
        tcb0 = "/ahb/apb/timer@f8008000";
        tcb1 = "/ahb/apb/timer@f800c000";
        i2c0 = "/ahb/apb/flexcom@f801c000/i2c@600";
        i2c1 = "/ahb/apb/flexcom@f8010000/i2c@600";
        serial2 = "/ahb/apb/flexcom@f8014000/serial@200";
    };

    cpus {
        #address-cells = <0x01>;
        #size-cells = <0x00>;

        cpu@0 {
            compatible = "arm,arm926ej-s";
            device_type = "cpu";
            reg = <0x00>;
        };
    };
};
```