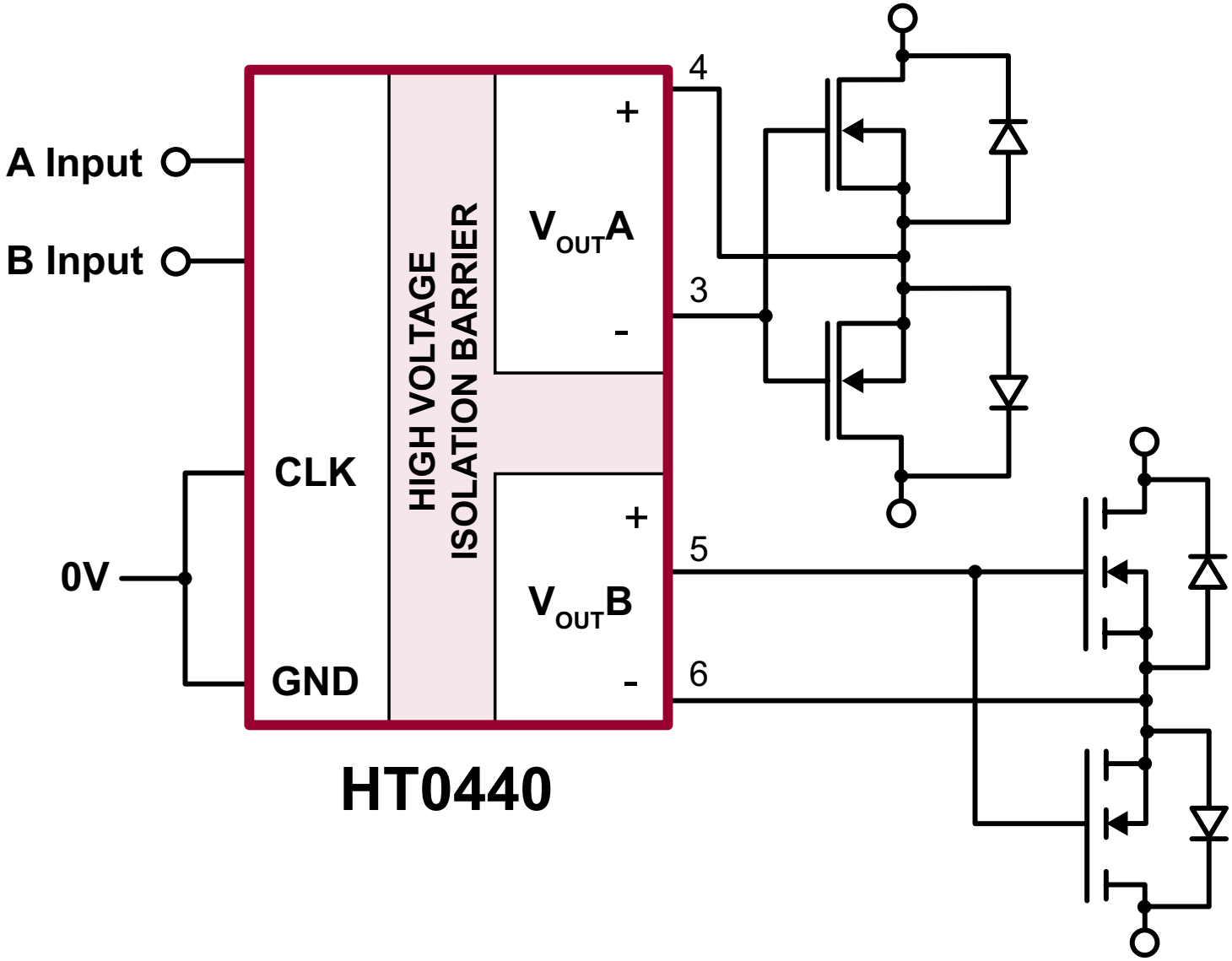
A Input
B Input
0V
CLK
GND
HIGH VOLTAGE
ISOLATION BARRIER
+
$V_{OUT}A$
-
+
$V_{OUT}B$
-
4
3
5
6
HT0440