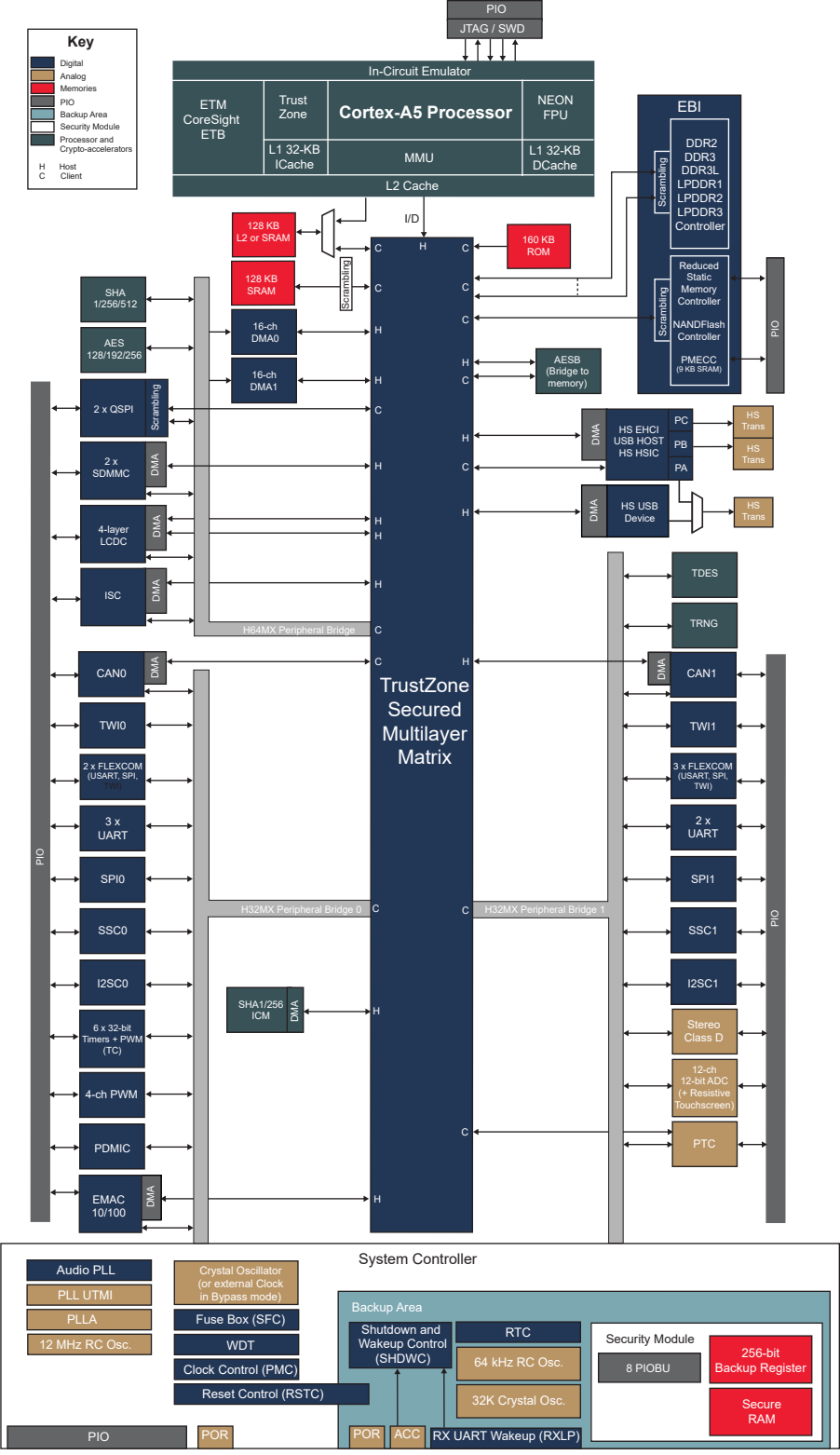
Key
Digital
Analog
Memories
PIO
Backup Area
Security Module
Processor and Crypto-accelerators
H Host
C Client
PIO
JTAG / SWD
In-Circuit Emulator
ETM CoreSight ETB
Trust Zone
Cortex-A5 Processor
NEON FPU
L1 32-KB ICache
MMU
L1 32-KB DCache
L2 Cache
I/D
128 KB L2 or SRAM
128 KB SRAM
Scrambling
160 KB ROM
EBI
DDR2 DDR3 DDR3L LPDDR1 LPDDR2 LPDDR3 Controller
Reduced Static Memory Controller
NANDFlash Controller
PMECC (9 KB SRAM)
SHA 1/256/512
AES 128/192/256
16-ch DMA0
16-ch DMA1
AESB (Bridge to memory)
2 x QSPI
2 x SDMMC
DMA
4-layer LCDC
ISC
HS EHCI USB HOST HS HSIC
PC
PB
PA
HS Trans
HS USB Device
H64MX Peripheral Bridge
TDES
TRNG
CAN0
CAN1
TWI0
TWI1
2 x FLEXCOM (USART, SPI, TWI)
3 x FLEXCOM (USART, SPI, TWI)
3 x UART
2 x UART
SPI0
SPI1
SSC0
SSC1
I2SC0
I2SC1
6 x 32-bit Timers + PWM (TC)
Stereo Class D
4-ch PWM
12-ch 12-bit ADC (+ Resistive Touchscreen)
PDMIC
PTC
EMAC 10/100
TrustZone Secured Multilayer Matrix
H32MX Peripheral Bridge 0
H32MX Peripheral Bridge 1
SHA1/256 ICM
System Controller
Audio PLL
PLL UTMI
PLLA
12 MHz RC Osc.
Crystal Oscillator (or external Clock in Bypass mode)
Fuse Box (SFC)
WDT
Clock Control (PMC)
Reset Control (RSTC)
Backup Area
Shutdown and Wakeup Control (SHDWC)
RTC
64 kHz RC Osc.
32K Crystal Osc.
Security Module
8 PIOBU
256-bit Backup Register
Secure RAM
PIO
POR
ACC
RX UART Wakeup (RXLP)