# AT24C16D: Package Marking Information

| 8-Lead PDIP | 8-Lead SOIC | 5-Lead SOT23 |
|:---:|:---:|:---:|
| ATMLUYWW<br>###%  CO<br>YYWWNNN | ATMLHYWW<br>###%  CO<br>YYWWNNN | ##%ÜYY<br>WWNNN |

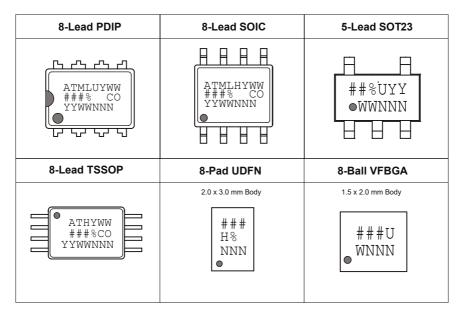| 8-Lead TSSOP | 8-Pad UDFN | 8-Ball VFBGA |
|:---:|:---:|:---:|
| | 2.0 x 3.0 mm Body | 1.5 x 2.0 mm Body |
| ATHYWW<br>###%CO<br>YYWWNNN | ###<br>H%<br>NNN | ###U<br>WNNN |

Note 1: ● designates pin 1
Note 2: Package drawings are not to scale
Note 3: For SOT23 package with date codes before 7B, the bottom line (YMXX) is marked on the bottom side, and there is no Country of Assembly (@) mark on the top line

| Catalog Number Truncation | | |
|---|---|---|
| AT24C16D | Truncation Code ###: 16D / ##: AD | |

| Date Codes | | Voltages |
|---|---|---|
| Y = Year Code (last digit of calendar year) | | % = Minimum Voltage |
| YY = Year Code (last 2 digits of calendar year) | | M: 1.7V min |
| WW = Week code (week of January 1 is week '01') | | |

| Country of Origin | Device Grade | Atmel Truncation |
|---|---|---|
| CO = Country of Origin | H or U:  Industrial Grade | AT:   Atmel<br>ATM:   Atmel<br>ATML:   Atmel |

| Trace Code |
|---|
| NNN = Alphanumeric Trace Code (2 Characters for Small Packages) |