

BANK 0		BANK 1		BANK 2		BANK 3		BANK 4		BANK 5		BANK 6		BANK 7	
000h	Core Registers	080h	Core Registers	100h	Core Registers	180h	Core Registers	200h	Core Registers	280h	Core Registers	300h	Core Registers	380h	Core Registers
00Bh		08Bh		10Bh		18Bh		20Bh		28Bh		30Bh		38Bh	
00Ch	PORTA	08Ch	PIR0	10Ch	—	18Ch	WDTCON	20Ch	—	28Ch	TMR1L	30Ch	T2TMR	38Ch	CCPR1L
00Dh	—	08Dh	PIR1	10Dh	—	18Dh	BORCON	20Dh	—	28Dh	TMR1H	30Dh	T2PR	38Dh	CCPR1H
00Eh	PORTC ⁽¹⁾	08Eh	PIR2	10Eh	—	18Eh	PCON0	20Eh	OSCCON2	28Eh	T1CON	30Eh	T2CON	38Eh	CCP1CON
00Fh	—	08Fh	PIR3	10Fh	—	18Fh	PCON1	20Fh	OSCCON3	28Fh	T1GCON	30Fh	T2HLT	38Fh	CCP1CAP
010h	—	090h	PIR4	110h	—	190h	—	210h	OSCSTAT	290h	T1GATE	310h	T2CLKCON	390h	CCPR2L
011h	—	091h	—	111h	—	191h	—	211h	OSCCON	291h	T1CLK	311h	T2RST	391h	CCPR2H
012h	TRISA	092h	—	112h	—	192h	—	212h	OSCTUNE	292h	TMR3L	312h	T4TMR	392h	CCP2CON
013h	—	093h	—	113h	—	193h	—	213h	OSCFRQ	293h	TMR3H	313h	T4PR	393h	CCP2CAP
014h	TRISC ⁽¹⁾	094h	—	114h	—	194h	—	214h	ACTCON	294h	T3CON	314h	T4CON	394h	—
015h	—	095h	—	115h	—	195h	—	215h	—	295h	T3GCON	315h	T4HLT	395h	—
016h	—	096h	PIE0	116h	—	196h	—	216h	—	296h	T3GATE	316h	T4CLKCON	396h	—
017h	—	097h	PIE1	117h	—	197h	—	217h	—	297h	T3CLK	317h	T4RST	397h	—
018h	LATA	098h	PIE2	118h	—	198h	—	218h	—	298h	—	318h	T6TMR	398h	—
019h	—	099h	PIE3	119h	—	199h	—	219h	—	299h	—	319h	T6PR	399h	—
01Ah	LATC ⁽¹⁾	09Ah	PIE4	11Ah	—	19Ah	—	21Ah	—	29Ah	—	31Ah	T6CON	39Ah	—
01Bh	—	09Bh	—	11Bh	—	19Bh	—	21Bh	—	29Bh	—	31Bh	T6HLT	39Bh	—
01Ch	—	09Ch	—	11Ch	—	19Ch	TMR0L	21Ch	—	29Ch	—	31Ch	T6CLKCON	39Ch	—
01Dh	—	09Dh	—	11Dh	—	19Dh	TRMOH	21Dh	—	29Dh	—	31Dh	T6RST	39Dh	—
01Eh	—	09Eh	—	11Eh	—	19Eh	TOCON0	21Eh	—	29Eh	—	31Eh	—	39Eh	—
01Fh	—	09Fh	—	11Fh	—	19Fh	TOCON1	21Fh	—	29Fh	—	31Fh	—	39Fh	CCPTMRS0
020h	General Purpose Registers 80 Bytes	0A0h	General Purpose Registers 80 Bytes	120h	General Purpose Registers 80 Bytes	1A0h	General Purpose Registers 80 Bytes ⁽²⁾	220h	General Purpose Registers 80 Bytes ⁽²⁾	2A0h	General Purpose Registers 80 Bytes ⁽²⁾	320h	General Purpose Registers 16 Bytes ⁽²⁾ Unimplemented Read as '0'	3A0h	Unimplemented Read as '0'
06Fh		0EFh		16Fh		1EFh		26Fh		2EFh		36Fh		3EFh	
070h	Common RAM (Accesses 70h-7Fh)	0F0h	Common RAM (Accesses 70h-7Fh)	170h	Common RAM (Accesses 70h-7Fh)	1F0h	Common RAM (Accesses 70h-7Fh)	270h	Common RAM (Accesses 70h-7Fh)	2F0h	Common RAM (Accesses 70h-7Fh)	370h	Common RAM (Accesses 70h-7Fh)	3F0h	Common RAM (Accesses 70h-7Fh)
07Fh		0FFh		17Fh		1FFh		27Fh		2FFh		37Fh		3FFh	

- Note:**
1. 14/16-pin devices only
 2. PIC16F18014/24 only

Legend:

■ Unimplemented data memory locations, read as '0'