


BANK 60

1E00h	Core Registers
1E0Bh	
1E0Ch	PPSLOCK
1E0Dh	INTPPS
1E0Eh	T0CKIPPS
1E0Fh	T1CKIPPS
1E10h	T1GPPS
1E11h	T3CKIPPS
1E12h	T3GPPS
1E13h	Unimplemented Read as '0'
1E18h	Unimplemented Read as '0'
1E19h	
1E1Ah	T2INPPS
1E1Ah	T4INPPS
1E1Bh	T6INPPS
1E1Ch	—
1E1Dh	—
1E1Eh	CCP1PPS
1E1Fh	CCP2PPS
1E20h	Unimplemented Read as '0'
1E3Ch	Unimplemented Read as '0'
1E3Dh	
1E3Dh	CLCIN0PPS
1E3Eh	CLCIN1PPS
1E3Fh	CLCIN2PPS
1E40h	CLCIN3PPS
1E41h	RX1PPS
1E42h	CK1PPS
1E43h	—
1E44h	—
1E45h	—
1E46h	—
1E47h	SSP1CLKPPS
1E48h	SSP1DATPPS
1E49h	SSP1SSPPS
1E4Ah	Unimplemented Read as '0'
1E4Fh	Unimplemented Read as '0'
1E50h	
1E50h	ADACTPPS
1E51h	Unimplemented Read as '0'
1E6Fh	Unimplemented Read as '0'
1E70h	
1E70h	Common RAM (Accesses 70h-7Fh)
1E7Fh	

Legend:

 Unimplemented data memory locations, read as '0'