BANK 56		BANK 57		BANK 58		BANK 59		BANK 60		BANK 61		BANK 62		BANK 63	
1C00h	Core Registers	1C80h	Core Registers	1D00h	Core Registers	1D80h	Core Registers	1E00h	Core Registers	1E80h	Core Registers	1F00h	Core Registers	1F80h	Core Registers
				1D0Bh											
1C0Bh		1C8Bh				1D8Bh		1E0Bh		1E8Bh		1F0Bh		1F8Bh	
1C0Ch	Unimplemented Read as '0'	1C8Ch	NVMADRL	1D0Ch	ADLTHL	1D8Ch	RA0PPS	1E0Ch	See Figure 9-12 for register mapping details	1E8Ch	See Figure 9-13 for register mapping details	1F0Ch	UMTOAP	1F8Ch	Unimplemented Read as '0'
		1C8Dh	NVMADRH	1D0Dh	ADLTHH	1D8Dh	RA1PPS					1F0Dh	UMTOAL		
		1C8Eh	NVMDATL	1D0Eh	ADUTHL	1D8Eh	RA2PPS					1F0Eh	UMTOAH		
		1C8Fh	NVMDATH	1D0Fh	ADUTHH	1D8Fh	—					1F0Fh	Unimplemented Read as '0'		
		1C90h	NVMCON1	1D10h	ADERRL	1D90h	RA4PPS								
		1C91h	NVMCON2	1D11h	ADERRH	1D91h	RA5PPS								
		1C92h	Unimplemented Read as '0'	1D12h	ADSTPTL	1D92h	Unimplemented Read as '0'								
				1D13h	ADSTPTH										
				1D14h	ADFLTRL										
				1D15h	ADFLTRH										
				1D16h	ADACCL										
				1D17h	ADACCH										
				1D18h	ADACCU										
				1D19h	ADCNT										
				1D1Ah	ADRPT										
				1D1Bh	ADPREVL	1D9Bh									
				1D1Ch	ADPREVH	1D9Ch	RC0PPS[1]								
				1D1Dh	ADRESL	1D9Dh	RC1PPS[1]								
				1D1Eh	ADRESH	1D9Eh	RC2PPS[1]								
				1D1Fh	ADPCH	1D9Fh	RC3PPS[1]								
				1D20h	ADNCH	1DA0h	RC4PPS[1]								
				1D21h	ADACQL	1DA1h	RC5PPS[1]								
				1D22h	ADACQH	1DA2h	Unimplemented Read as '0'								
				1D23h	ADCAP										
				1D24h	ADPREL										
				1D25h	ADPREH										
				1D26h	ADCON0										
				1D27h	ADCON1										
				1D28h	ADCON2										
				1D29h	ADCON3									1FE3h	
				1D2Ah	ADSTAT									1FE4h	STATUS_SHAD
				1D2Bh	ADREF									1FE5h	WREG_SHAD
				1D2Ch	ADACT									1FE6h	BSR_SHAD
				1DCDh	ADCLK									1FE7h	PCLATH_SHAD
				1D2Eh	ADCG1A									1FE8h	FSR0L_SHAD
				1D2Fh	—									1FE9h	FSR0H_SHAD
				1D30h	ADCG1C[1]									1FEAh	FSR1L_SHAD
				1D31h	Unimplemented Read as '0'									1FEBh	FSR1H_SHAD
														1FECh	—
														1FEDh	STKPTR
														1FEEh	TOSL
1C6Fh		1CEFh		1D6Fh		1DEFh		1E6Fh		1EEFh		1F6Fh		1FEFh	TOSH
1C70h	Common RAM (Accesses 70h-7Fh)	1CF0h	Common RAM (Accesses 70h-7Fh)	1D70h	Common RAM (Accesses 70h-7Fh)	1DF0h	Common RAM (Accesses 70h-7Fh)	1E70h	Common RAM (Accesses 70h-7Fh)	1EF0h	Common RAM (Accesses 70h-7Fh)	1F70h	Common RAM (Accesses 70h-7Fh)	1FF0h	Common RAM (Accesses 70h-7Fh)
1C7Fh		1CFFh		1D7Fh		1DFFh		1E7Fh		1EFFh		1F7Fh		1FFFh	

Note: **1. 14/16-pin devices only**

Legend:

Unimplemented data memory locations, read as '0'