BANK 40	BANK 41	BANK 42	BANK 43	BANK 44	BANK 45	BANK 46	BANK 47
1400h Core Registers	1480h Core Registers	1500h Core Registers	1580h Core Registers	1600h Core Registers	1680h Core Registers	1700h Core Registers	1780h Core Registers
140Bh	148Bh	150Bh	158Bh	160Bh	168Bh	170Bh	178Bh
140Ch Unimplemented Read as '0'	148Ch Unimplemented Read as '0'	150Ch Unimplemented Read as '0'	158Ch Unimplemented Read as '0'	160Ch Unimplemented Read as '0'	168Ch Unimplemented Read as '0'	170Ch Unimplemented Read as '0'	178Ch Unimplemented Read as '0'
146Fh	14EFh	156Fh	15EFh	166Fh	16EFh	176Fh	17EFh
1470h Common RAM (Accesses 70h-7Fh) 147Fh	14F0h Common RAM (Accesses 70h-7Fh) 14FFh	1570h Common RAM (Accesses 70h-7Fh) 157Fh	15F0h Common RAM (Accesses 70h-7Fh) 15FFh	1670h Common RAM (Accesses 70h-7Fh) 167Fh	16F0h Common RAM (Accesses 70h-7Fh) 16FFh	1770h Common RAM (Accesses 70h-7Fh) 177Fh	17F0h Common RAM (Accesses 70h-7Fh) 17FFh

Legend:
Unimplemented data memory locations, read as '0'