

BANK 24		BANK 25		BANK 26		BANK 27		BANK 28		BANK 29		BANK 30		BANK 31	
C00h	Core Registers	C80h	Core Registers	D00h	Core Registers	D80h	Core Registers	E00h	Core Registers	E80h	Core Registers	F00h	Core Registers	F80h	Core Registers
C0Bh	—	C8Bh	—	D0Bh	Unimplemented Read as '0'	D8Bh	Unimplemented Read as '0'	E0Bh	Unimplemented Read as '0'	E8Bh	Unimplemented Read as '0'	F0Bh	Unimplemented Read as '0'	F8Bh	Unimplemented Read as '0'
C0Ch															
C0Dh															
C0Eh															
C0Fh															
C10h															
C11h															
C12h															
C13h															
C14h															
C15h															
C16h															
C17h															
C18h															
C19h															
C1Ah															
C1Bh															
C1Ch															
C1Dh															
C1Eh															
C1Fh															
C20h	Unimplemented Read as '0'	C9Fh	Unimplemented Read as '0'	CA0h	Unimplemented Read as '0'	DEFh	Unimplemented Read as '0'	E70h	Unimplemented Read as '0'	EEFh	Unimplemented Read as '0'	F70h	Unimplemented Read as '0'	FEFh	Unimplemented Read as '0'
C6Fh															
C70h															
C7Fh															
Common RAM (Accesses 70h-7Fh)															
Common RAM (Accesses 70h-7Fh)															
C7Fh	Common RAM (Accesses 70h-7Fh)	CF0h	Common RAM (Accesses 70h-7Fh)	D70h	Common RAM (Accesses 70h-7Fh)	DF0h	Common RAM (Accesses 70h-7Fh)	E70h	Common RAM (Accesses 70h-7Fh)	EF0h	Common RAM (Accesses 70h-7Fh)	F70h	Common RAM (Accesses 70h-7Fh)	FF0h	Common RAM (Accesses 70h-7Fh)
C7Fh	Common RAM (Accesses 70h-7Fh)	CFh	Common RAM (Accesses 70h-7Fh)	D7Fh	Common RAM (Accesses 70h-7Fh)	DFh	Common RAM (Accesses 70h-7Fh)	E7Fh	Common RAM (Accesses 70h-7Fh)	EFh	Common RAM (Accesses 70h-7Fh)	F7Fh	Common RAM (Accesses 70h-7Fh)	FFh	Common RAM (Accesses 70h-7Fh)

Legend:

Unimplemented data memory locations, read as '0'